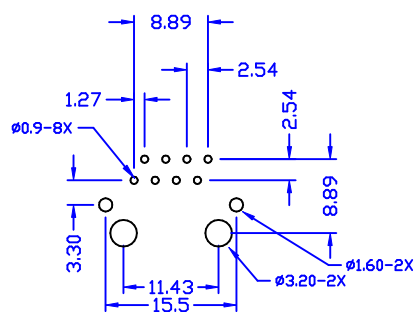
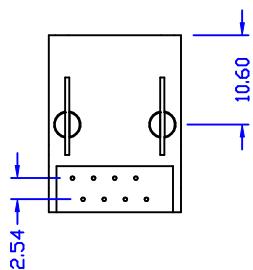
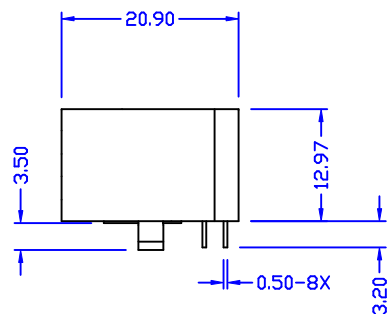
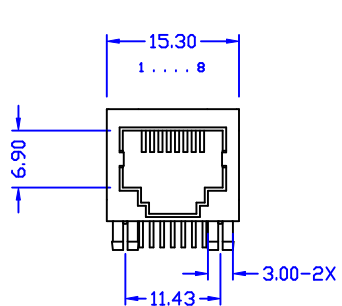
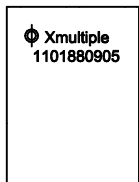
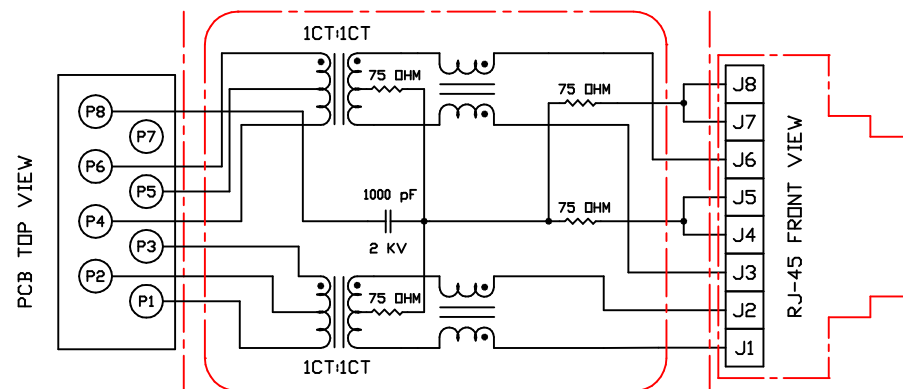




PCB LAYOUT MOUNTING HOLE(TOP VIEW)

UNLESS OTHERWISE SPECIFIED ALL TOLERANCES ARE ± 0.25

BLOCK CIRCUIT $\times 1$ SET IN ATM-073D



ELECTRICAL CHARACTERISTICS :

1. $\square$ CL (@100KHZ/0.1Vrms 8mA DC/BIAS) : 350 μ H MIN.
2. TURNS RATIO : TX = 1CT1CT $\pm 5\%$.
RX = 1CT1CT $\pm 5\%$.
3. HI-POT : 1500 Vrms.
4. INSERTION LOSS: 1~100MHZ = -1.0 dB MAX.
5. RETURN LOSS : 1~30MHZ = -20 dB MIN.
30~60MHZ = -14 dB MIN.
60~80MHZ = -13 dB MIN.
6. CROSS TALK : 1~100MHZ = -30 dB MIN.
7. CMRR : 1~100MHZ = -30 dB MIN.

						Atech® Atech Technology Co., Ltd. TEL:022794-6667 FAX:022794-8004	TITLE 10/100 BASE-LAN-MATE	
							DWG. NO. 1101880905	
	RELEASE	10/26/2004	BART	AL	SUEDY	UNITS: M/M	SAFETY	SHEET 10F 1
NO.	DESCRIPTION	DATE	BY	CHK	APPD	DATE 10/26/04		
REVISIONS								