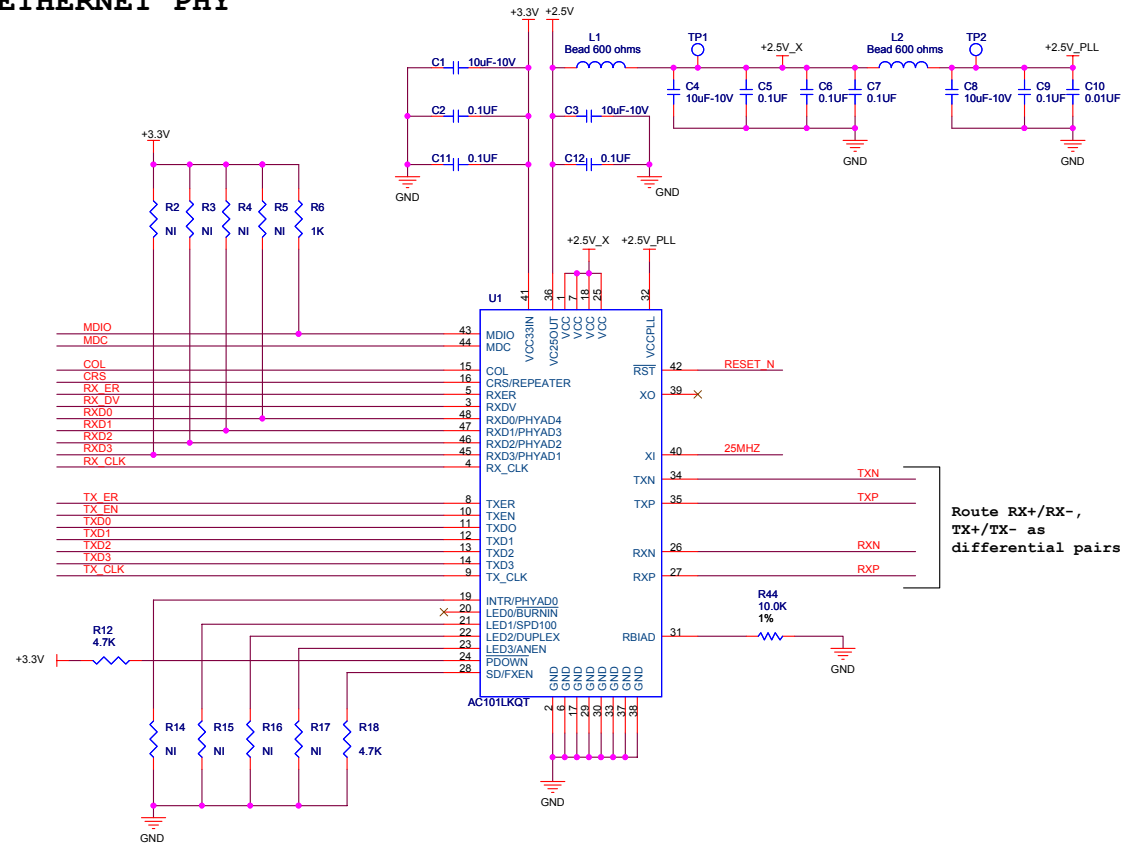
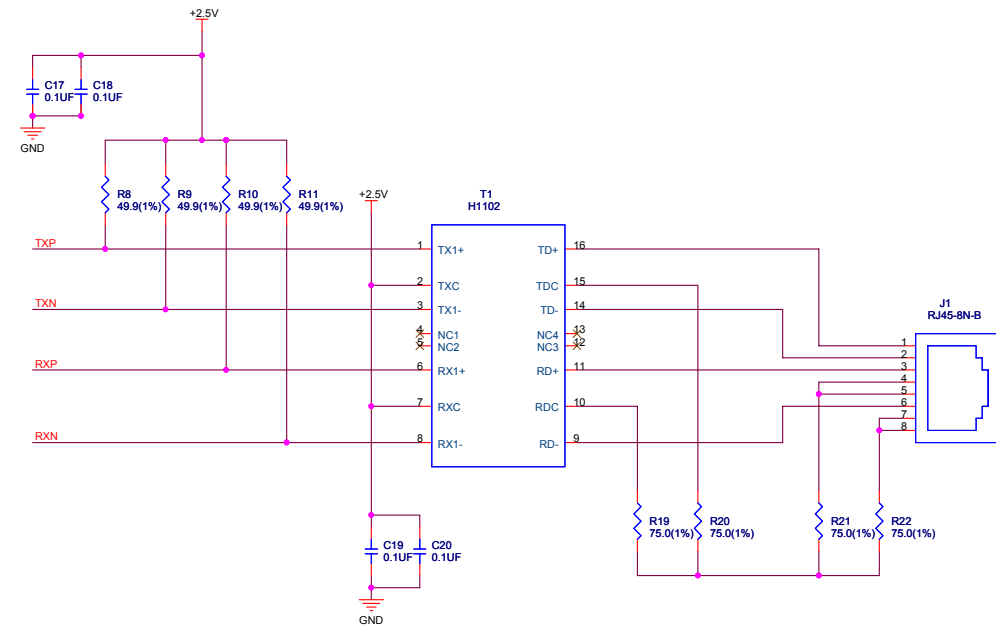


REVISION DESCRIPTION AND APPROVAL				
REV	ECO#	CHANGE DESCRIPTION	CHECKED	DATE
2	1752	FCC, UL RECOMMENDATIONS		

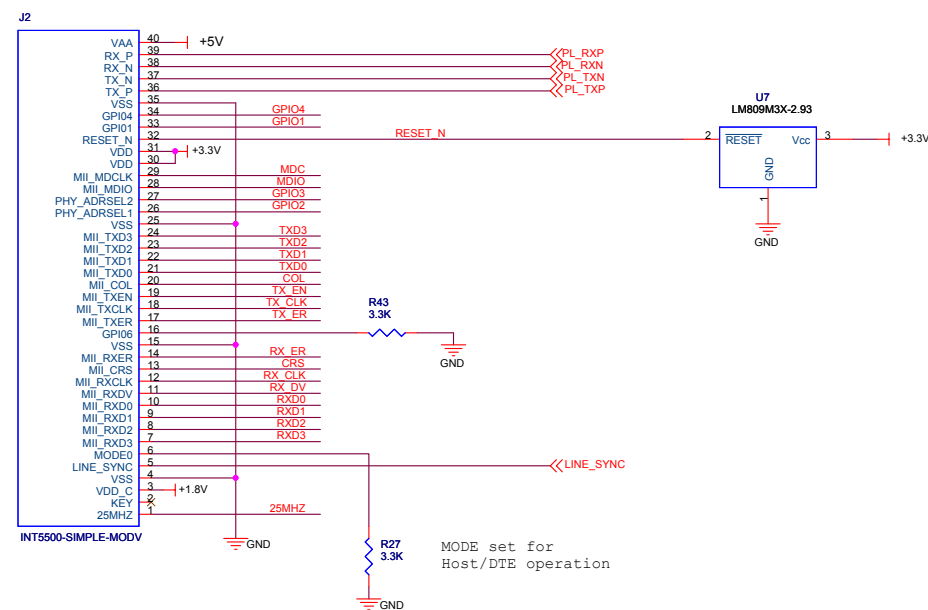
ETHERNET PHY



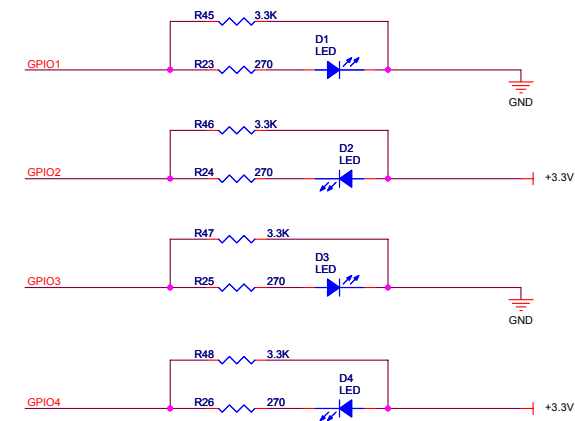
ETHERNET INTERFACE



INT5500, MII SIMPLE INTERFACE

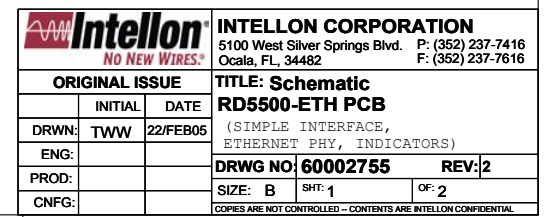


INDICATORS



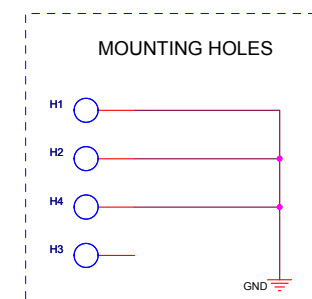
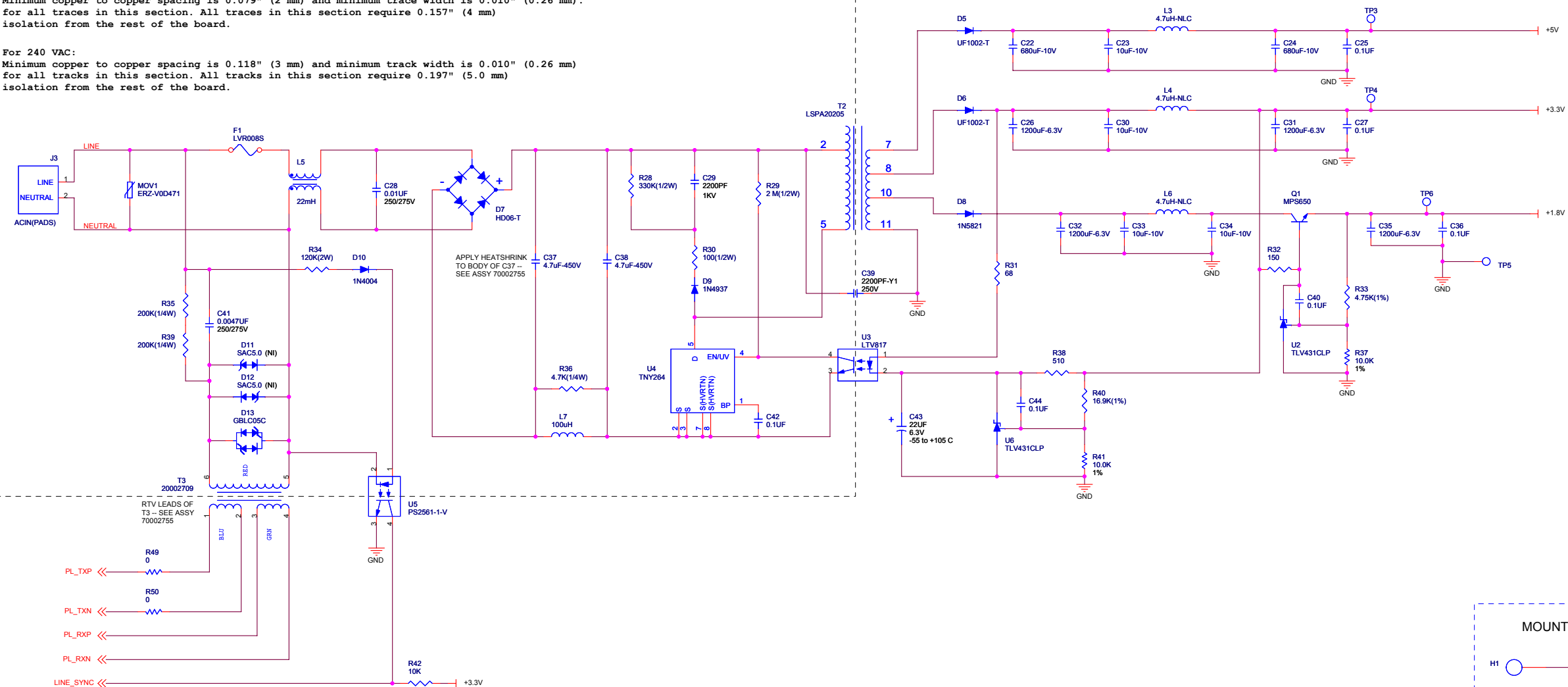
INDICATOR DESCRIPTIONS

```
GPI01.....PL-ACT
GPI02.....ETH-ACT
GPI03.....TURBO
GPI04.....POWER
GPIO6.....NOT USED
```



For 120 VAC:
Minimum copper to copper spacing is 0.079" (2 mm) and minimum trace width is 0.010" (0.26 mm).
for all traces in this section. All traces in this section require 0.157" (4 mm)
isolation from the rest of the board.

For 240 VAC:
Minimum copper to copper spacing is 0.118" (3 mm) and minimum track width is 0.010" (0.26 mm)
for all tracks in this section. All tracks in this section require 0.197" (5.0 mm)
isolation from the rest of the board.



INTELLON CORPORATION		
5100 West Silver Springs Blvd. Ocala, FL, 34482		P: (352) 237-7416 F: (352) 237-7616
TITLE: Schematic		
RD5500-ETH PCB		
(SWITCHING POWER SUPPLY)		
DRWG NO:	60002755	REV: 2
SIZE: B	SHT: 2	OF: 2
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