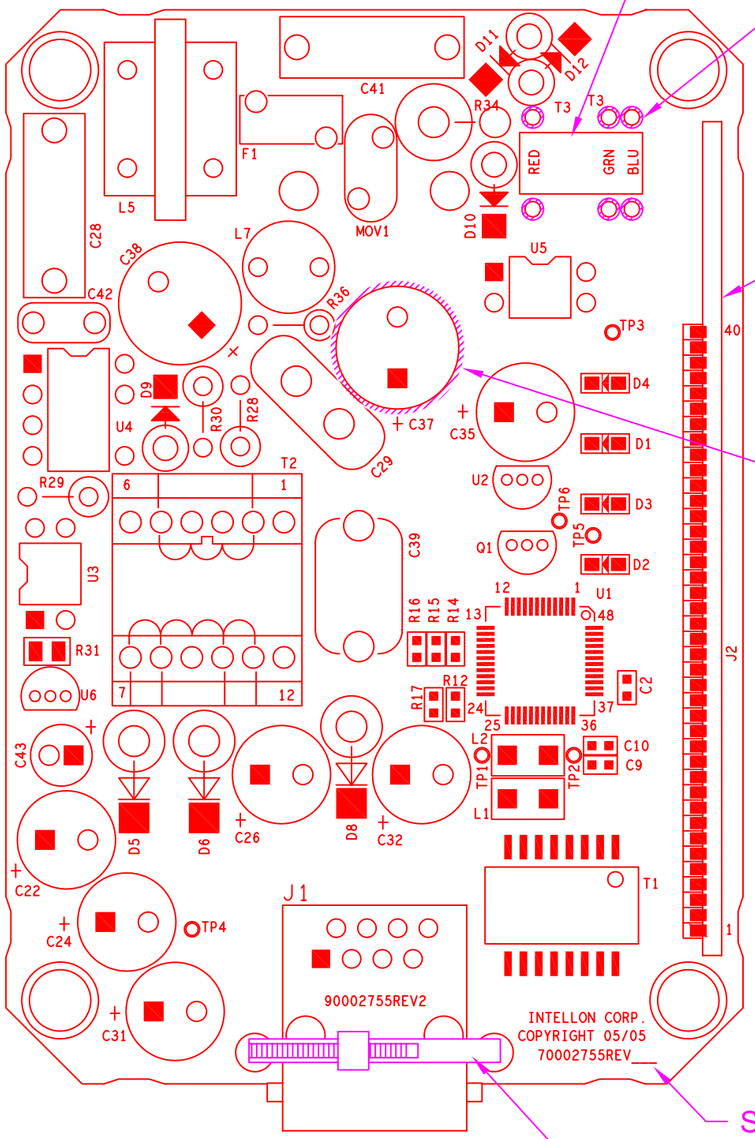
REVISION					
REV	DESCRIPTION	APPROVED	DATE	CONFIG	DATE
2	ECO #1752 ADDED TIEWRAP HOLES J2				

TOP SIDE



74 20002709 TRANSFORMER

69 RTV

APPLY RTV TO ALL LEADS OF T3 TO ENSURE LEADS CAN NOT MOVE

4 INT55MX 10002754-01

70 HEATSHRINK

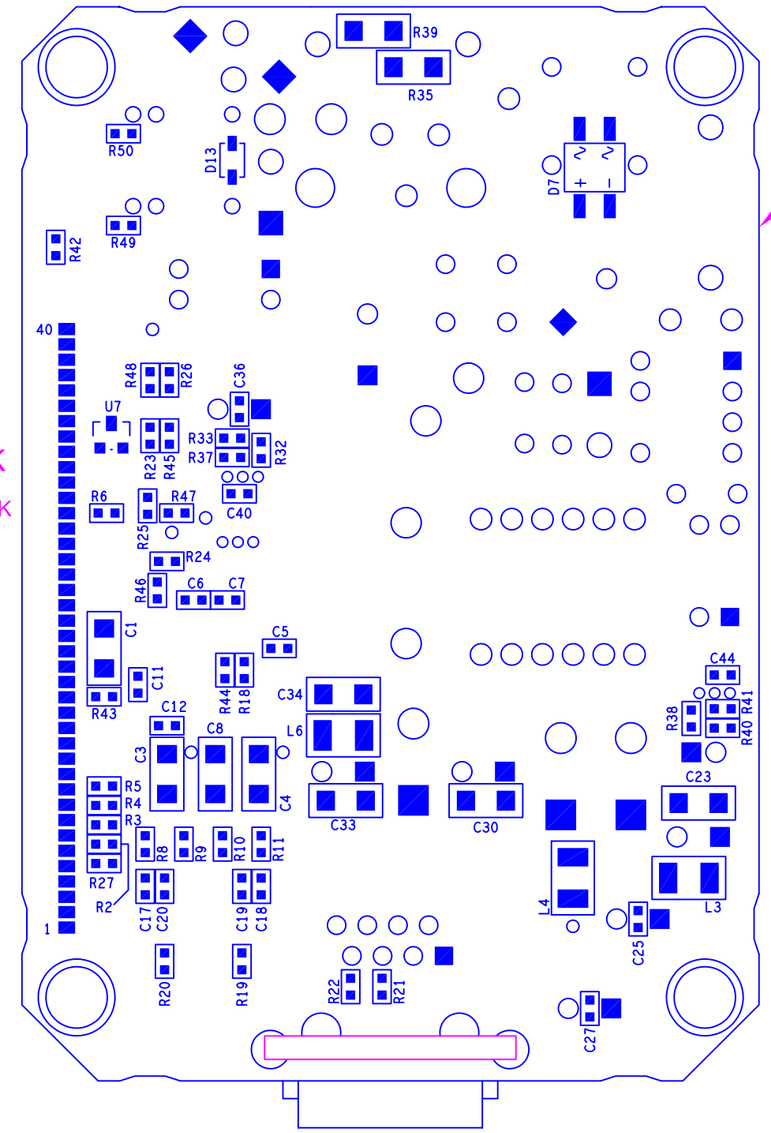
APPLY 1" HEATSHRINK AROUND C37 TOP TO BOTTOM

SEE NOTE 2

68 TIEWRAP

SECURE J1 WITH TIEWRAP AS SHOWN

BOTTOM SIDE



2 80002755 PCB

NOTES: UNLESS OTHERWISE SPECIFIED;

1. CIRCUIT BOARD ASSEMBLY TO BE IN ACCORDANCE WITH ANSI/IPC-A-610A, CLASS 2.

2. MARK THE REVISION OF THIS ASSEMBLY DRAWING WITH PERMANENT INK IN THE SPACE PROVIDED ON THE BOARD.

3. SOME COMPONENTS MAY NOT BE SHOWN IN SOME VIEWS FOR CLARITY.

4. SEE GERBER ZIP FILE (90002755.zip) FOR PLACED COMPONENT FILE.

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XXXXXXXX	10002755
NEXT ASSY	USED ON
APPLICATION	

UNLESS OTHERWISE SPECIFIED:
DIMENSIONS ARE IN INCHES
TOLERANCES ARE:
FRACTIONS ± 1/64 DECIMALS .XX ± 0.01 .XXX ± 0.005 ANGLES ± 1/2°

MATERIAL:

FINISH:

CONTRACT NO:		
APPROVALS		DATE
DRAWN:	LASBY	3/29/05
CHECKED:		
ENG:		
OPER:		
CM:		
DO NOT SCALE PRINTS		

Intellon No New Wires.
INTELLON CORPORATION
5100 SILVER SPRINGS BLVD.
OCALA, FL 34482
PHONE: (352) 237-7416 FAX (352) 237-7616
http://www.Intellon.com

ASSEMBLY
RD5500-ETH PCB

SIZE	FSCM NO:	DRAWING NO:	REV:
B		70002755	2

SCALE: 2:1 SHEET 1 OF 1