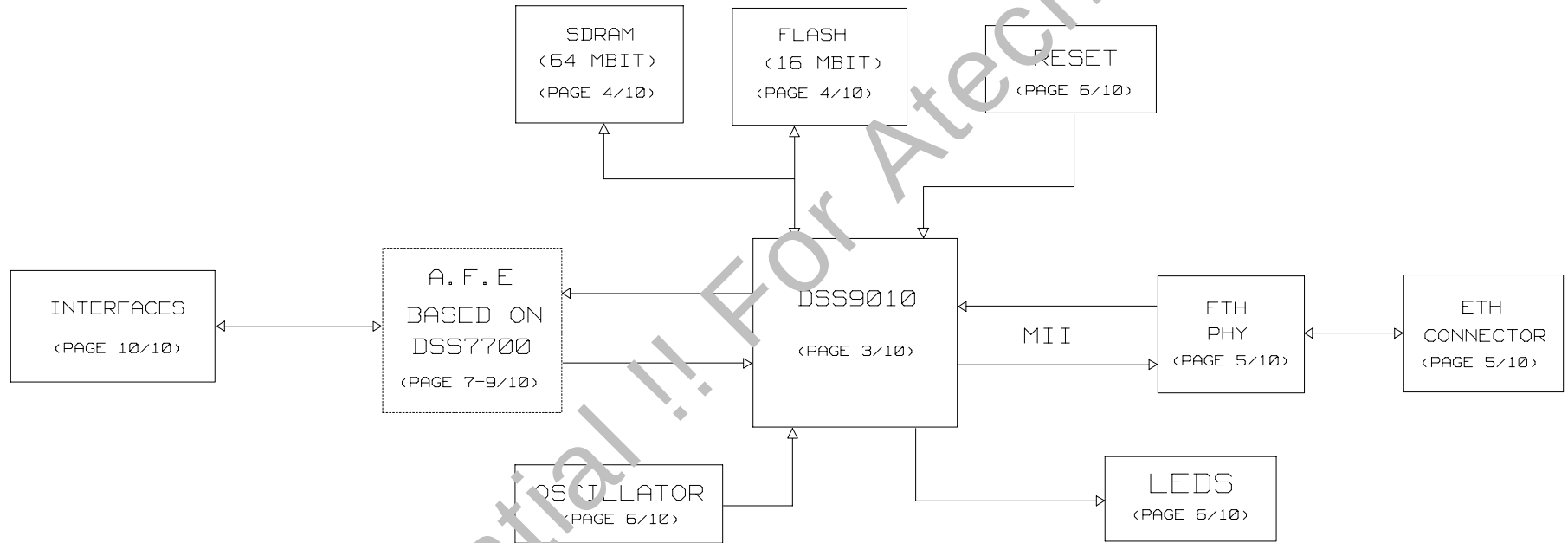


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CONFIDENTIAL DS2 DESIGN OF SYSTEMS ON SILICON PARQUE TECNOLÓGICO C/ CHARLES ROBERT DARWIN, 2 46980 - PATERNÀ (VALENCIA) SPAIN TEL.: +34 96 136 60 04 FAX: +34 96 136 62 50 WWW.DS2.ES PROJECT: SB10P SIGNAL CARD OF DH10P DOC. CODE: A26Y10002-SC-01 TITLE: INDEX REV. 1.0 AUTHOR: PEDRO MARTINEZ DATE: 27/04/2005 REVISED BY: JOSE LUIS GONZALEZ DATE: 28/04/2005 APPROVED BY: FRANCISCO JOSE ANDRES DATE: 28/04/2005 THIS DOCUMENT CONTAINS INFORMATION PROPRIETARY TO DESIGN OF SYSTEMS ON SILICON (DS2). USE OF DISCLOSURE WITHOUT THE WRITTEN PERMISSION OF AN OFFICER OF DS2 IS EXPRESSLY FORBIDDEN. PAGE: 1/10							
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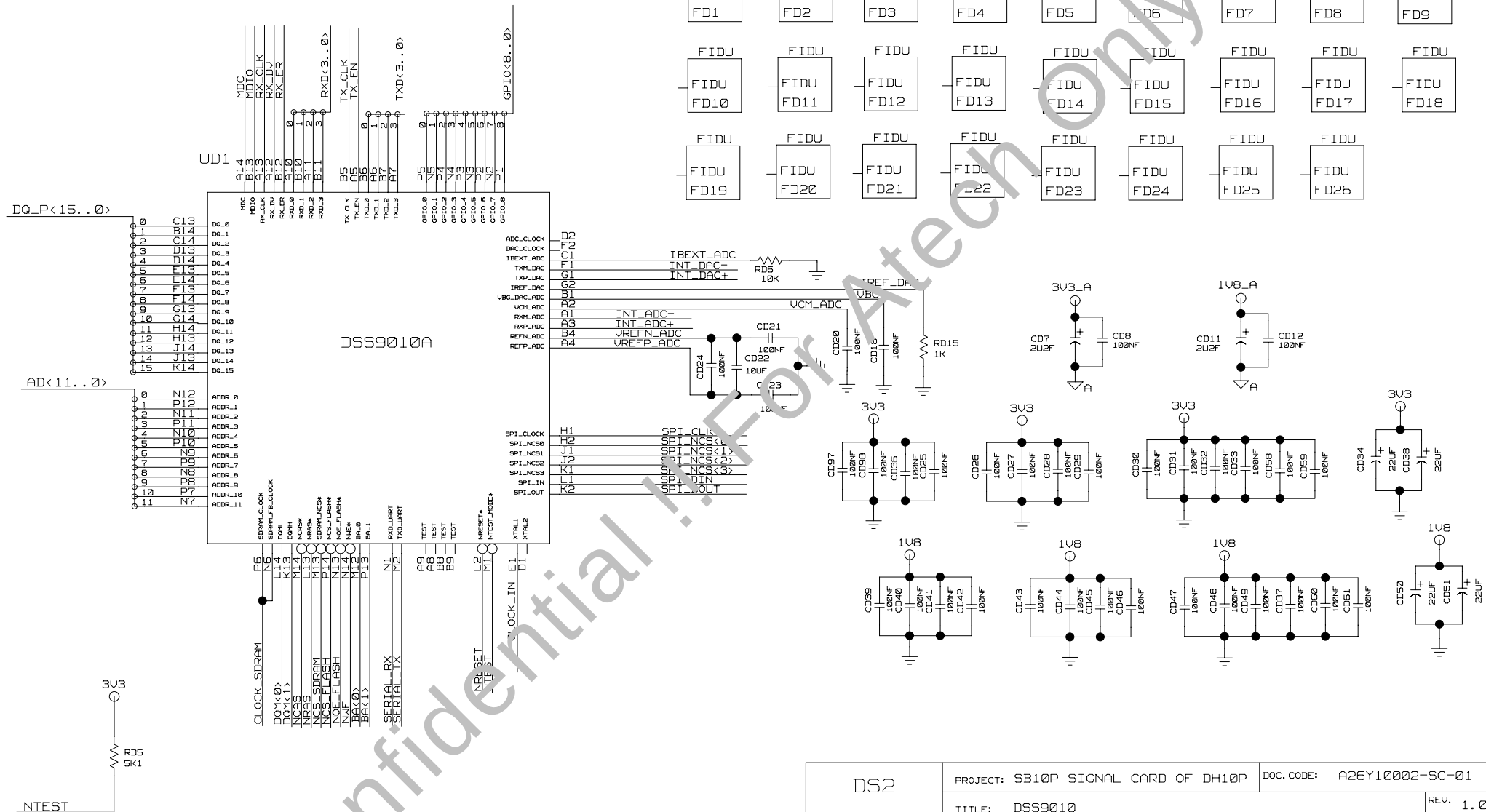
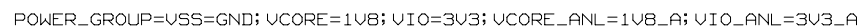
DIGITAL BLOCK DIAGRAM



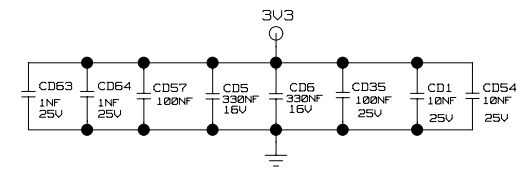
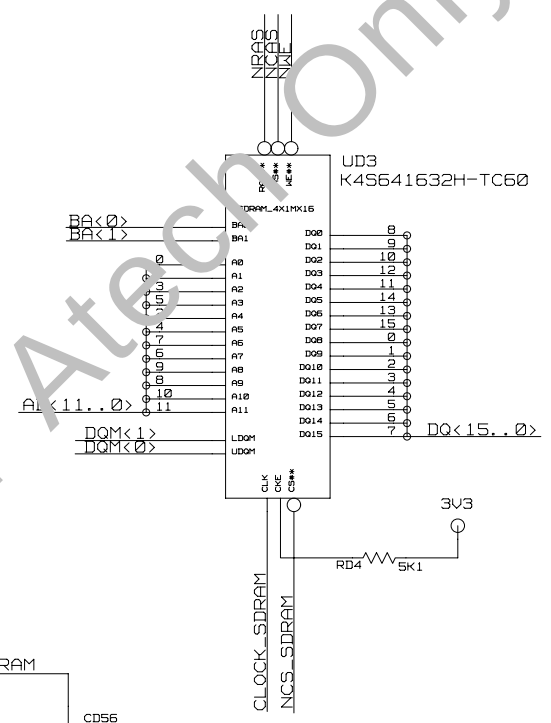
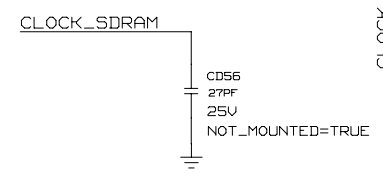
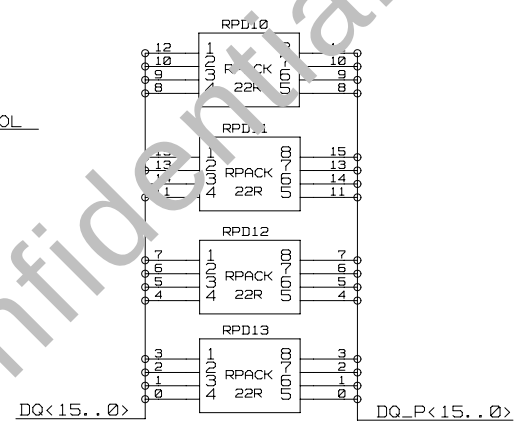
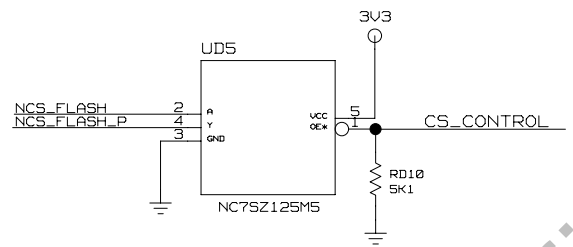
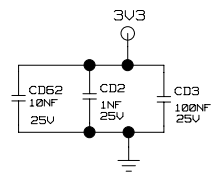
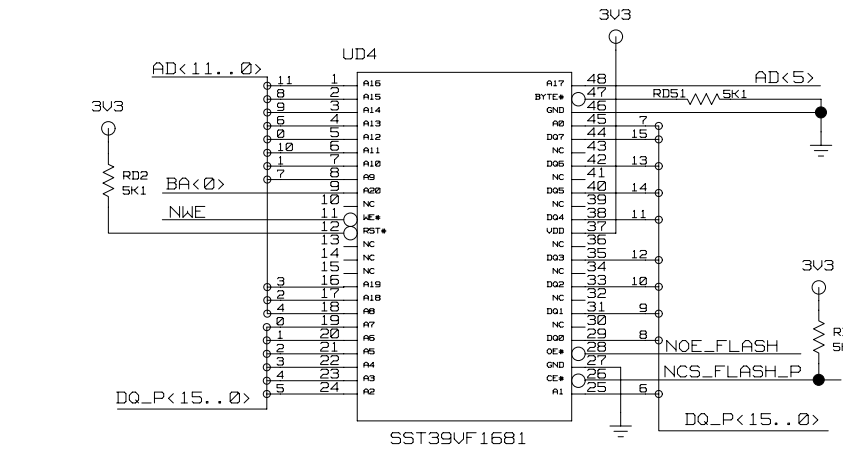
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PROJECT: SB10P SIGNAL CARD OF DH10P		DOC. CODE: A26Y10002-SC-01	
TITLE: DIGITAL FRONT END BLOCK DIAGRAM			REV. 1.0
AUTHOR: PEDRO MARTINEZ		DATE: 27/04/2005	
REVISED BY: JOSE LUIS GONZALEZ		DATE: 28/04/2005	
APPROVED BY: FRANCISCO J. ANDRES		DATE: 28/04/2005	
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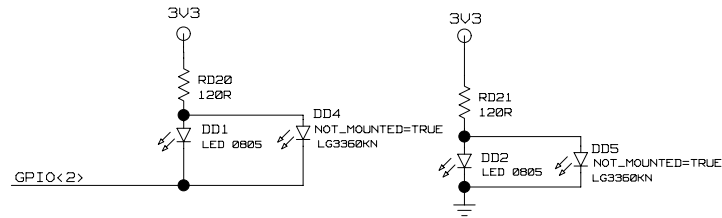


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	TITLE: DSS9010			REV. 1.0
	AUTHOR: REBECA MARTINEZ / J. CAMPS		DATE: 27/04/2005	
	REVISED BY: DARIO BECARES		DATE: 28/04/2005	
	APPROVED BY: HAKAN FOUREN		DATE: 28/04/2005	
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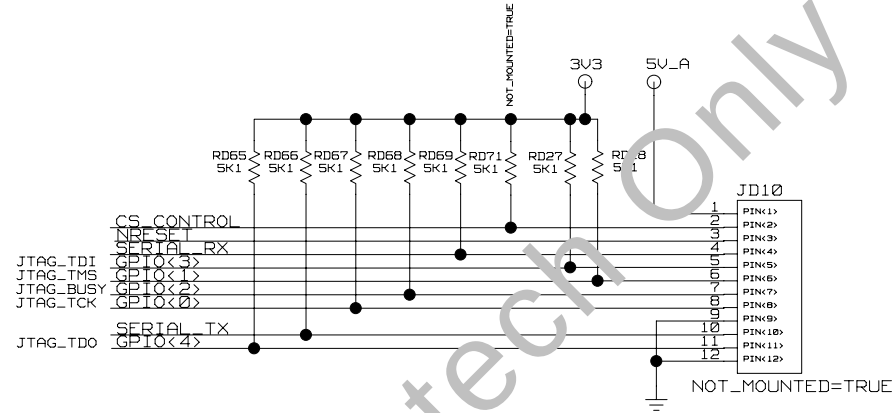


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	TITLE: MEMORIES			REV. 1.0
	AUTHOR: CHEMA OGARA / J. CAMPS		DATE: 27/04/2005	
	REVISED BY: DARIO BECARES		DATE: 28/04/2005	
	APPROVED BY: HAKAN FOUREN		DATE: 28/04/2005	
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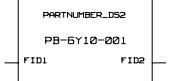
PLC LINK/ACTIVITY



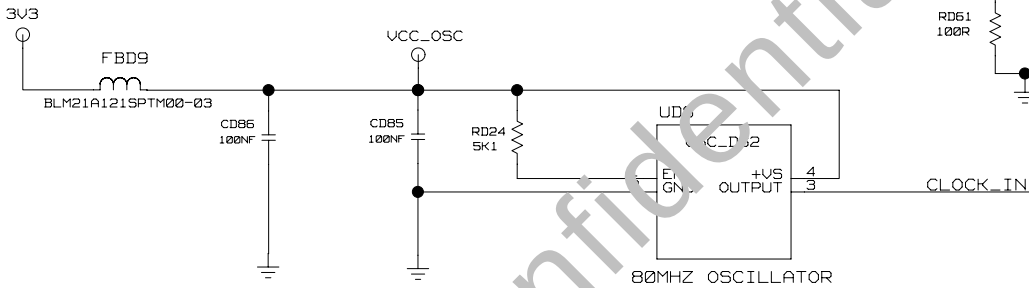
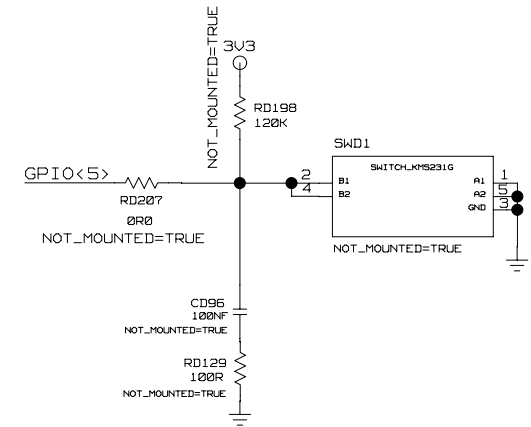
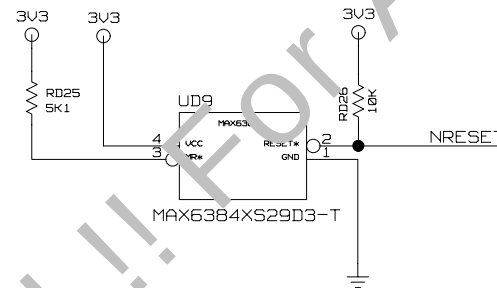
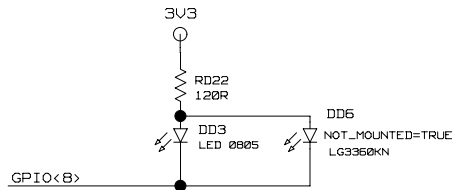
PLC POWER



PB-6Y10-001



ETH LINK/ACTIVITY

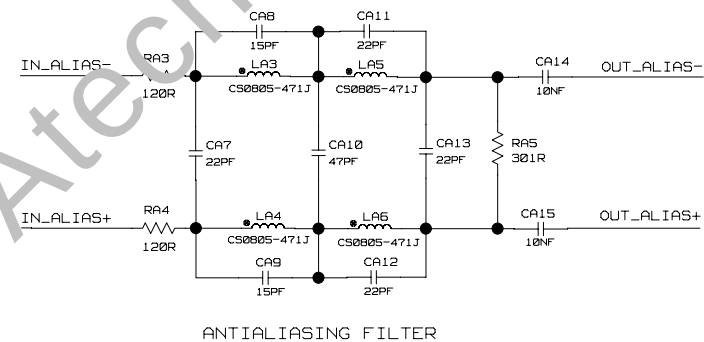
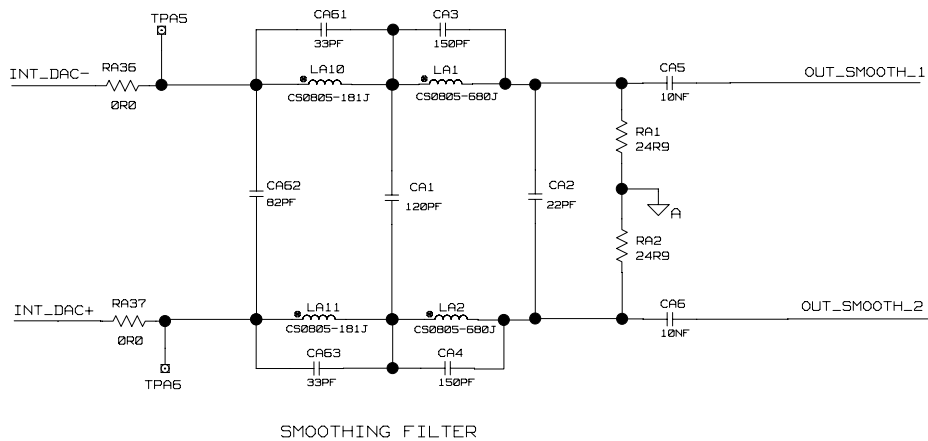


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PROJECT: SB10P SIGNAL CARD OF DH10P	DOC. CODE: A26Y1002-SC-01
TITLE: RESET & LEDS	REV. 1.0
AUTHOR: REBECA MARTINEZ / J.L.CAMPS	DATE: 27/04/2005
REVISED BY: DARIO BECARES	DATE: 28/04/2005
APPROVED BY: HAKAN FOUREN	DATE: 28/04/2005
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PAGE: 6/10	

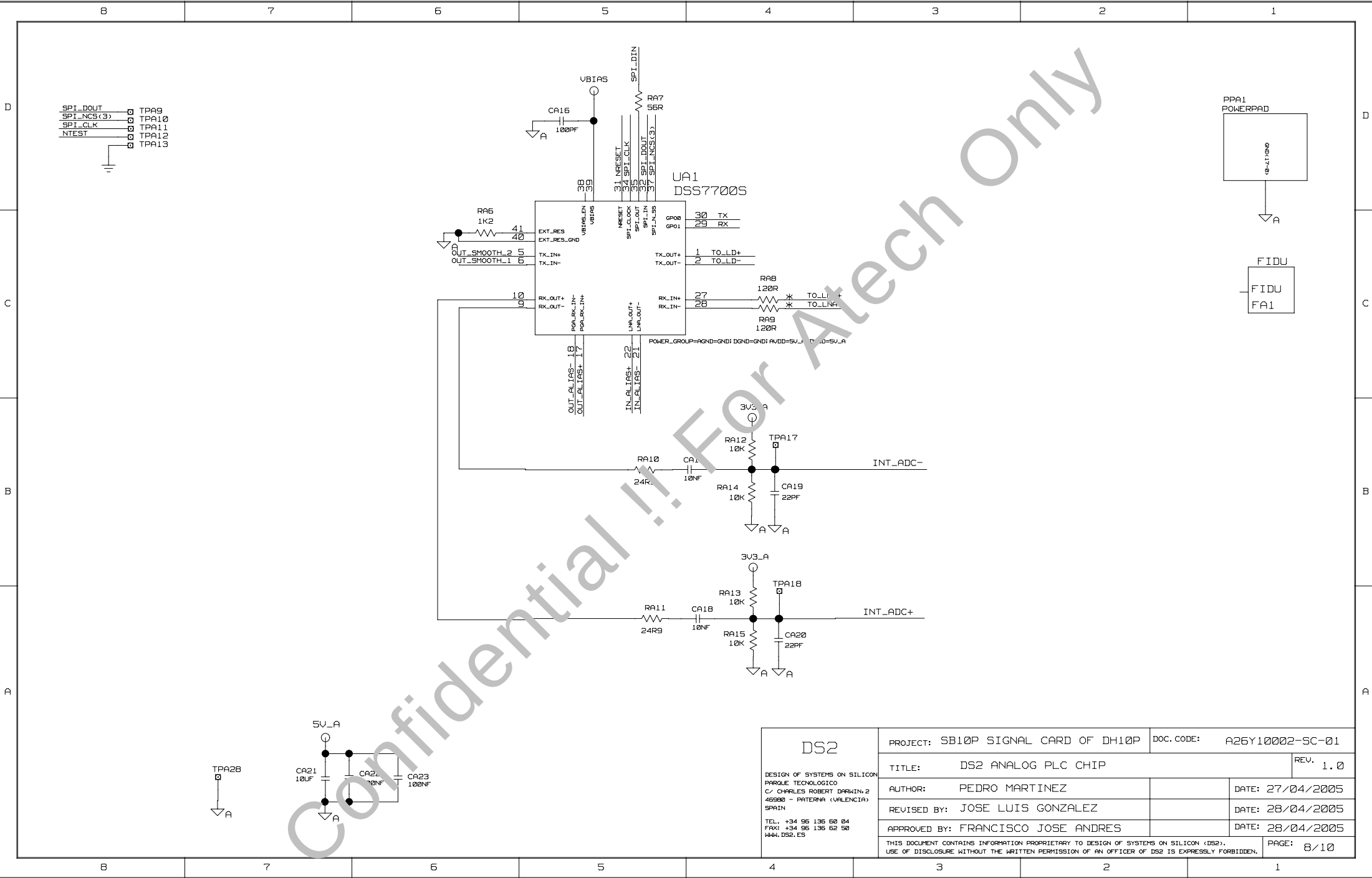


DS2

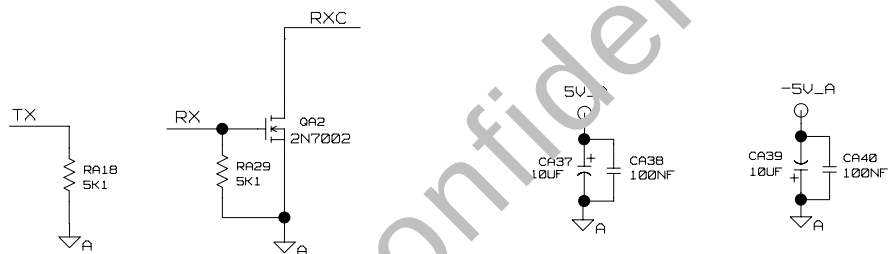
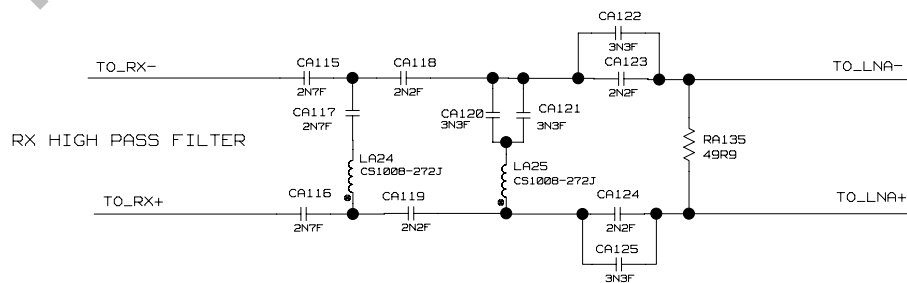
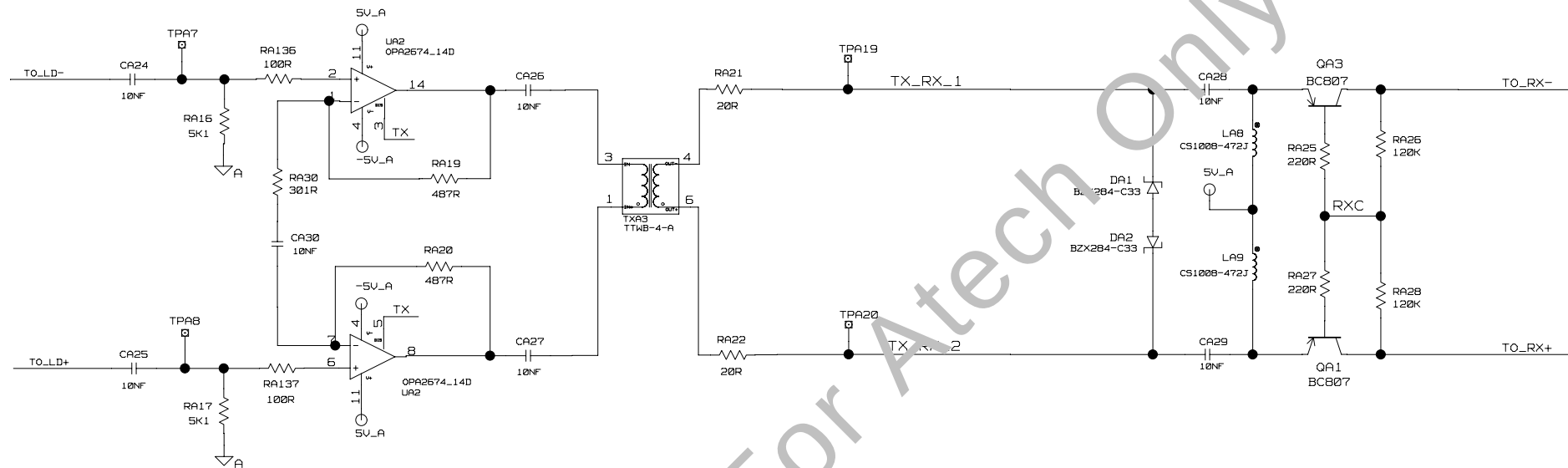
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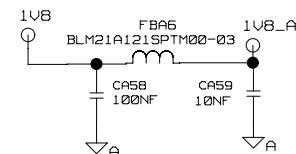
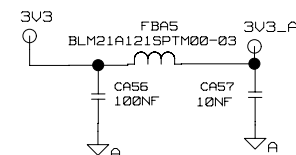
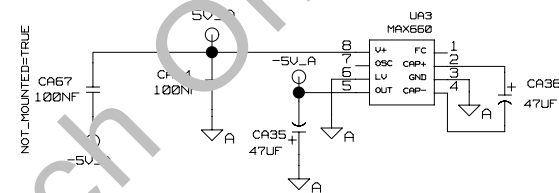
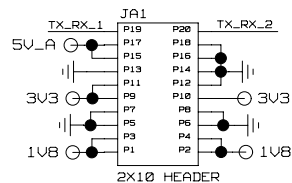
PROJECT: SB10P SIGNAL CARD OF DH10P	DOC. CODE: A26Y10002-SC-01
TITLE: FILTERS	REV. 1.0
AUTHOR: PEDRO MARTINEZ / J. CAMPS	DATE: 27/04/2005
REVISED BY: JOSE LUIS GONZALEZ	DATE: 28/04/2005
APPROVED BY: FRANCISCO JOSE ANDRES	DATE: 28/04/2005
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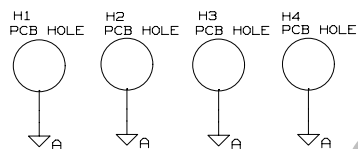
DS2 DESIGN OF SYSTEMS ON SILICON PARQUE TECNOLÓGICO C/ CHARLES ROBERT DARWIN, 2 46500 - PATERNÀ (VALENCIA) SPAIN TEL.: +34 96 136 60 04 FAX: +34 96 136 62 50 WWW.DS2.ES	PROJECT: SB10P SIGNAL CARD OF DH10P		DOC. CODE: A26Y10002-SC-01	
	TITLE: DS2 ANALOG PLC CHIP			REV. 1.0
	AUTHOR: PEDRO MARTINEZ		DATE: 27/04/2005	
	REVISED BY: JOSE LUIS GONZALEZ		DATE: 28/04/2005	
	APPROVED BY: FRANCISCO JOSE ANDRES		DATE: 28/04/2005	
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	TITLE: LINE DRIVER & SWITCHES			REV. 1.0
	AUTHOR: JOSE LUIS CAMPS		DATE: 27/04/2005	
	REVISED BY: JOSE LUIS GONZALEZ		DATE: 28/04/2005	
	APPROVED BY: FRANCISCO JOSE ANDRES		DATE: 28/04/2005	
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PCB HOLES FOR HEATSINK FIXING



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PROJECT: SB10P SIGNAL CARD OF DH10P	DOC. CODE: A26Y10002-SC-01
TITLE: POWER SUPPLY / INTERCONNECTIONS	REV. 1.0
AUTHOR: PEDRO MARTINEZ	DATE: 27/04/2005
REVISED BY: J.M. VIDAL / A. POVEDA	DATE: 28/04/2005
APPROVED BY: FRANCISCO JOSE ANDRES	DATE: 28/04/2005
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