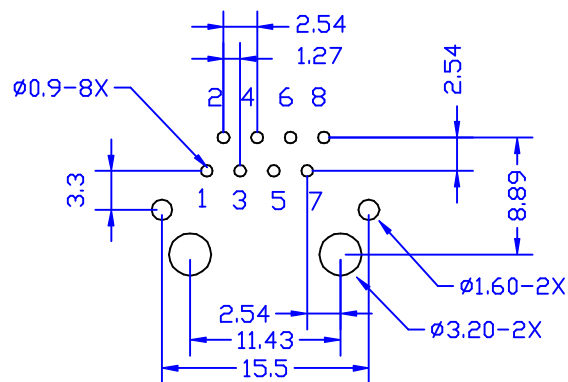
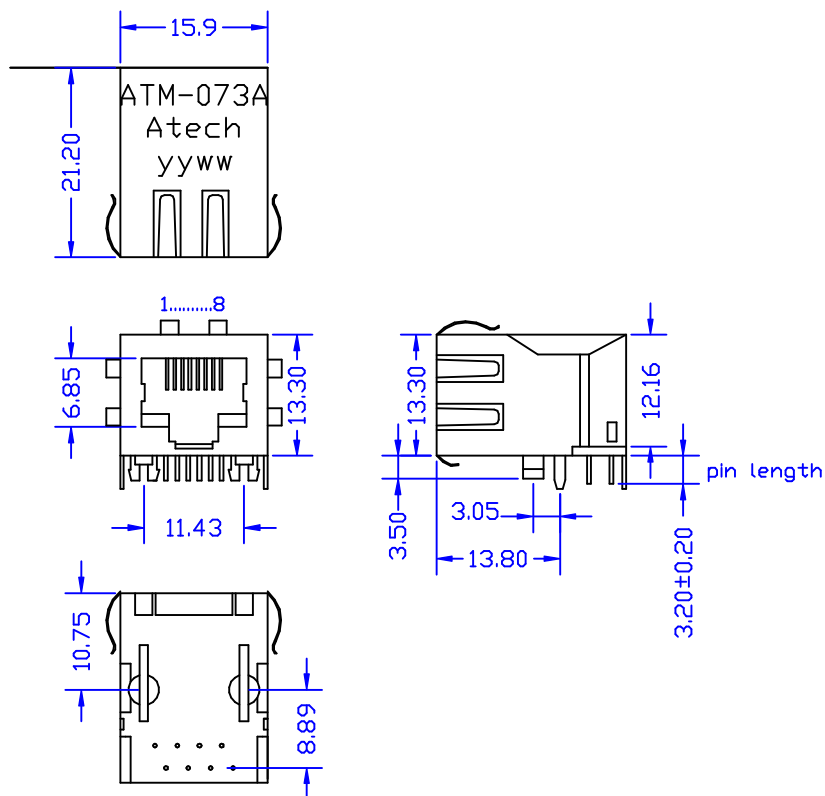
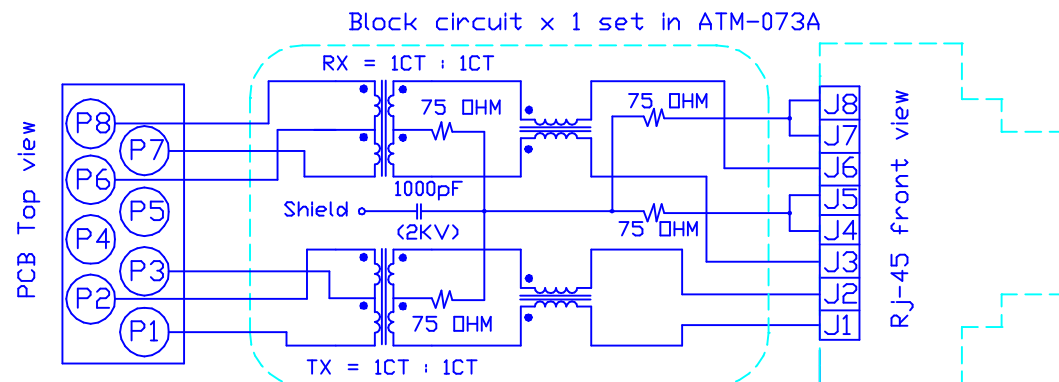




PCB Layout mounting hole(top view)

TOL:±/-0.25



ELECTRICAL CHARACTERISTICS :

- TURN RATIOS : (P8-P7):(J6-J3)=1CT:1CT&(P2-P1):(J2-J1)=1CT:1CT
- INDUCTANCE : (P8-P7):350uH MIN @100KHZ 100mV/8mA BIAS
(P2-P1):350uH MIN @100KHZ 100mV/8mA BIAS
- CW/W : (P6,P7,P8):(J6-J3)=28,(P2,P3,P1):(J2-J1)=28pF MAX.
- LEAKAGE INDUCTANCE(uH MAX.) : P8-P7(WITH J6 AND J3 SHORT):0.4
P2-P1(WITH J2 AND J1 SHORT):0.4
- HI-POT TEST : 1500Vrms/0.5mA 1SEC.
- INSERTION LOSS(1-100MHZ) : -1.15dB MAX.
- RETURN LOSS (dB MIN) : 30MHZ 40MHZ 50MHZ 60MHZ 80MHZ
-16 -14 -13.5 -13 -10
- CROSS TALK(0.1-60)MHZ-40dB,(60-100)MHZ : -38dB
- COMMON MODE REJECTION RATIO : (0.1-60)MHZ:-45dB XMIT/-40dB RCV
(60-100)MHZ:-40dB XMIT/-35dB RCV

					Atech®		TITLE 10/100 BASE-T LAN-MATE	
					Atech Technology Co., Ltd.		DWG. NO.	
					TEL:(02)2794-6667 FAX:(02)2794-8004		ATM-073A	
RELEASE	04/26/2001	DOLLY	SUEDY	UNITS:	SAFETY		SHEET 1 OF 1	
NO:	DESCRIPTION	DATE	BY	CHK	APPD	DATE	DRAW	
REVISIONS					04/26/01		Pair	