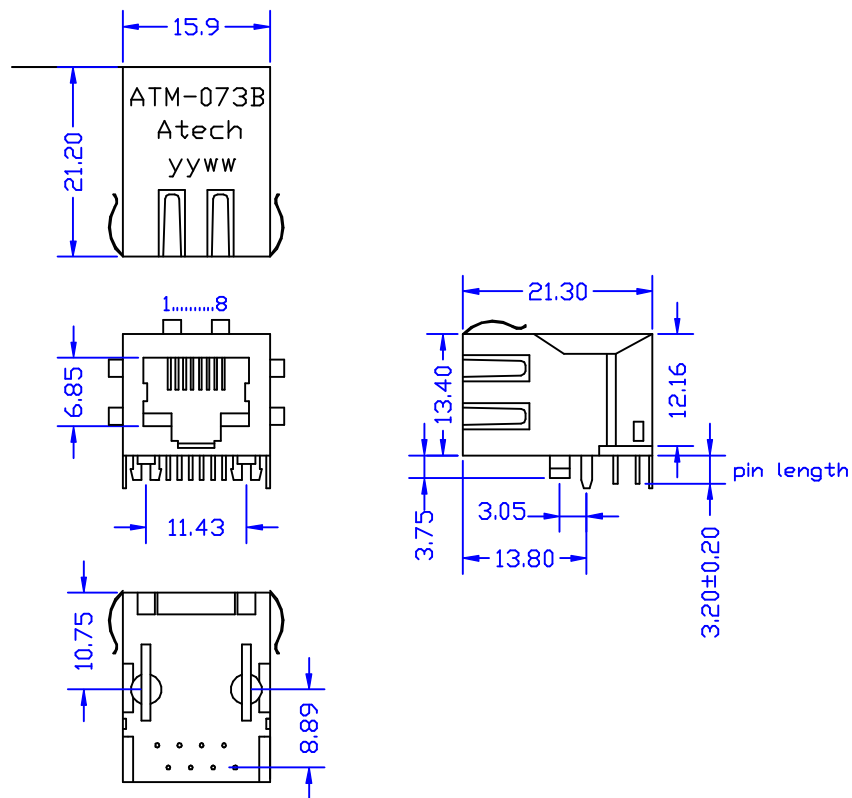
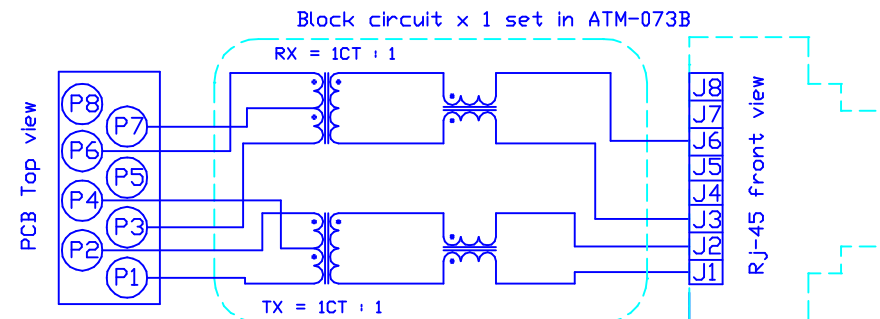




PCB Layout mounting hole (top view)

TOL: ± 0.25



ELECTRICAL CHARACTERISTICS:

- TURN RATIOS: $\langle P6-P3 \rangle \langle J6-J3 \rangle = 1CT:1CT$ & $\langle P2-P1 \rangle \langle J2-J1 \rangle = 1CT:1CT$
- INDUCTANCE: $\langle P6-P3 \rangle \geq 350\mu H$ MIN @100KHZ 100mV/8mA BIAS
 $\langle P2-P1 \rangle \geq 350\mu H$ MIN @100KHZ 100mV/8mA BIAS
- CW/W: $\langle P6, P7, P3 \rangle \langle J6-J3 \rangle = 28$, $\langle P2, P4, P1 \rangle \langle J2-J1 \rangle = 28pF$ MAX.
- LEAKAGE INDUCTANCE (μH MAX.): $P6-P3$ (WITH $J6$ AND $J3$ SHORT): 0.4
 $P2-P1$ (WITH $J2$ AND $J1$ SHORT): 0.4
- HI-POT TEST: 1500Vrms/0.5mA 1SEC.
- INSERTION LOSS (1-100MHZ): $\leq 1.15dB$ MAX.
- RETURN LOSS (dB MIN):

	30MHZ	40MHZ	50MHZ	60MHZ	80MHZ
	-16	-14	-13.5	-13	-10
- CROSS TALK (0.1-60)MHZ: $\leq 40dB$, (60-100)MHZ: $\leq 38dB$
- COMMON MODE REJECTION RATIO: (0.1-60)MHZ: $\leq 45dB$ XMIT/ $\leq 40dB$ RCV
 (60-100)MHZ: $\leq 40dB$ XMIT/ $\leq 35dB$ RCV

						Atech® Atech Technology Co., Ltd. TEL:(02)2794-6667 FAX:(02)2794-8004	TITLE 10/100 BASE-T LAN-MATE	
							DWG. NO. ATM-073B	
	RELEASE	07/16/2001	DOLLY		SUEDY	UNITS: M/M	SAFETY	SHEET 1 OF 1
NO.	DESCRIPTION	DATE	BY	CHK	APPD	DATE 07/16/01		DRAW <i>Pair</i>
REVISIONS								