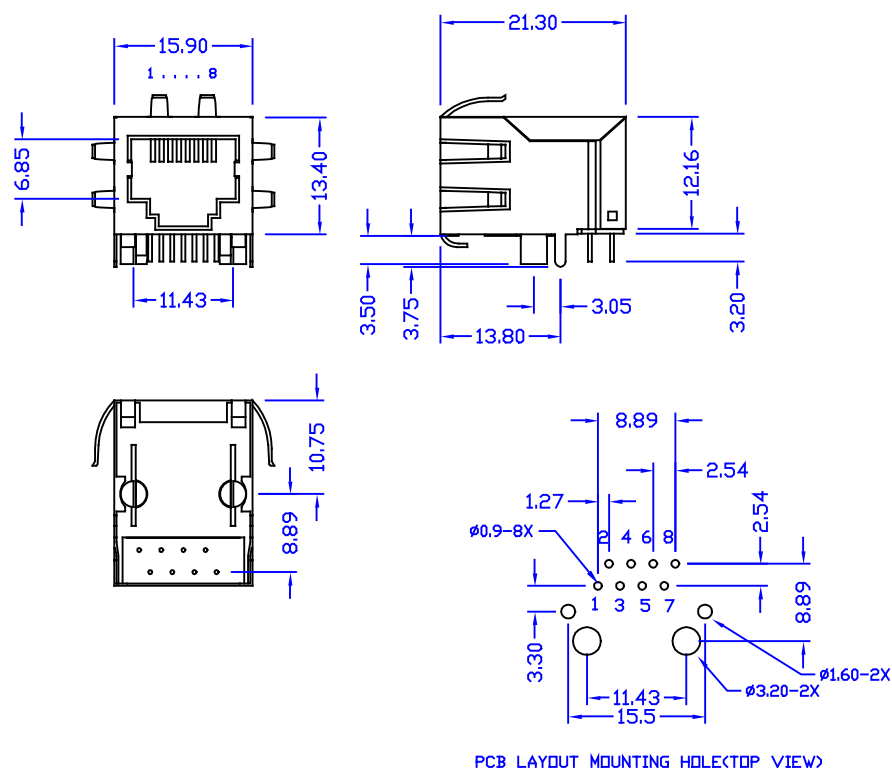
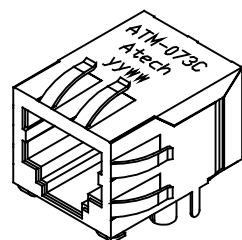
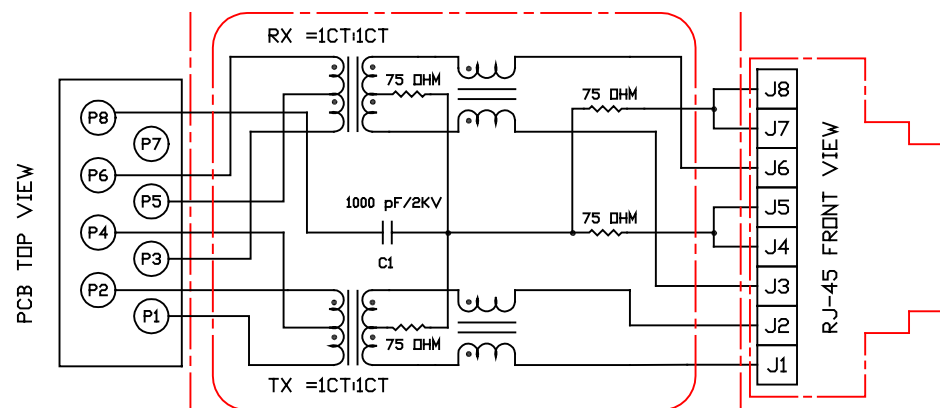




UNLESS OTHERWISE SPECIFIED ALL TOLERANCES ARE $\pm 0,25$

BLOCK CIRCUIT x 1 SET IN ATM-073C



ELECTRICAL CHARACTERISTICS :

1. TURNS RATIO : TX = 1CT:1CT $\pm 5\%$.
RX = 1CT:1CT $\pm 5\%$.
2. INDUCTANCE (@100KHZ/0.1Vrms WITH 8mA DC BIAS) : 350 μ H MIN.
3. CW/W : 28 pF MAX.
4. LEAKAGE INDUCTANCE : 0.4 μ H MAX.
5. INSERTION LOSS : 1~100MHZ = -1.5 dB MAX.
6. VOLTAGE ISOLATION : 1500 Vrms.
7. RETURN LOSS : 30MHZ = -16 dB MIN.
40MHZ = -14 dB MIN.
50MHZ = -13.5 dB MIN.
60MHZ = -13 dB MIN.
80MHZ = -10 dB MIN.
8. CROSS TALK : 0.1~60MHZ = -40 dB MIN.
60~100MHZ = -38 dB MIN.
9. COMMON MODE REJECTION :
0.1~60MHZ TX/RX = -45/-40 dB MIN.
60~100MHZ TX/RX = -40/-35 dB MIN.

						 Atech [®] Atech Technology Co., Ltd. TEL:022794-6667 FAX:022794-8004		TITLE 10/100 BASE-T LAN-MATE	
								DWG. NO. ATM-073C	
	RELEASE	07/16/2004	BART	AL	SUEDY	UNITS: M/M	SAFETY	SHEET 1 OF 1	
NO.	DESCRIPTION	DATE	BY	CHK	APPD	DATE		DRAW <i>Pair</i>	
REVISIONS						0716/04			