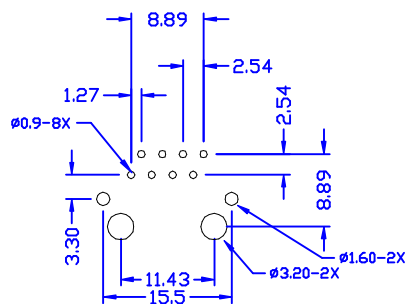
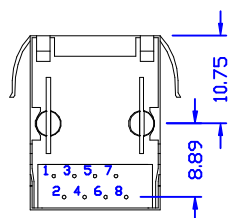
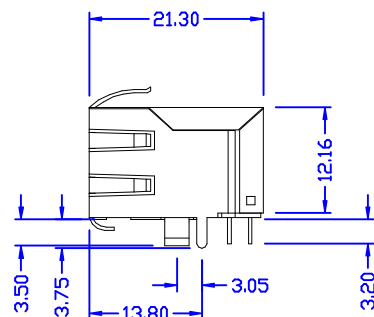
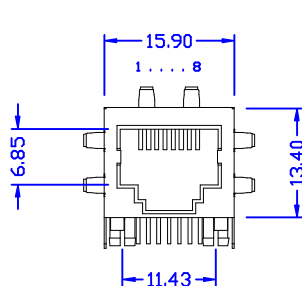
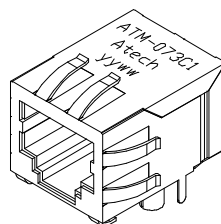
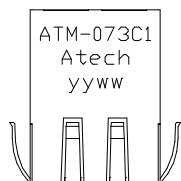
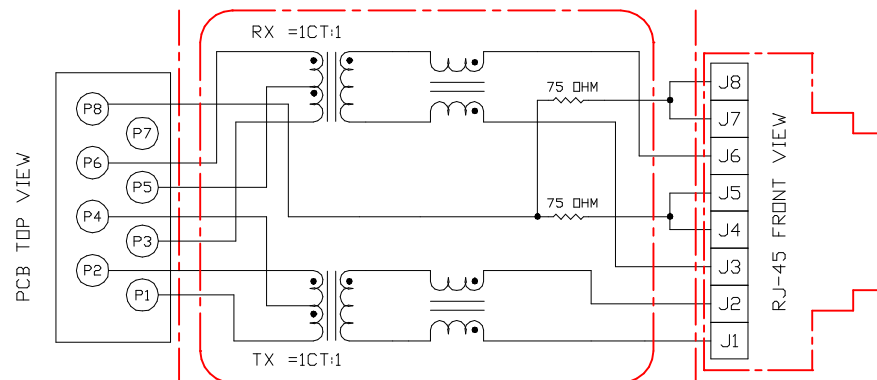




PCB LAYOUT MOUNTING HOLE(TOP VIEW)

UNLESS OTHERWISE SPECIFIED ALL TOLERANCES ARE ± 0.25

BLOCK CIRCUIT $\times 1$ SET IN ATM-073C1



ELECTRICAL CHARACTERISTICS :

1. TURNS RATIO : TX = 1CT:1 $\pm 5\%$.
RX = 1CT:1 $\pm 5\%$.
2. INDUCTANCE (@100KHZ/0.1Vrms WITH 8mA DC BIAS) : 350 uH MIN.
3. CMRR : 0.1~100MHZ = -40 dB TYP.
4. CROSS TALK : 0.1~100MHZ = -40 dB MIN.
5. VOLTAGE ISOLATION : 1500 Vrms.

						Atech®		TITLE 10/100 BASE-T LAN-MATE
A	PIN NO.	02/02 /2005	BART	AL	SUEDY	Atech Technology Co., Ltd. TEL:0202794-6667 FAX:0202794-8804		DWG. NO. ATM-073C1
	RELEASE	09/06 /2004	BART	AL	SUEDY	UNITS: M/M	SAFETY	SHEET 10F 1
NO.	DESCRIPTION	DATE	BY	CHK	APPD	DATE 09/06/04		DRAW Pair
REVISIONS								