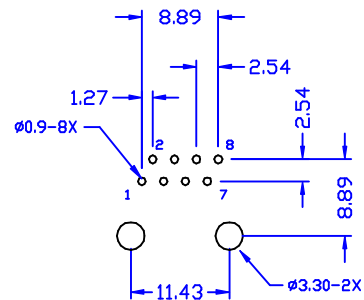
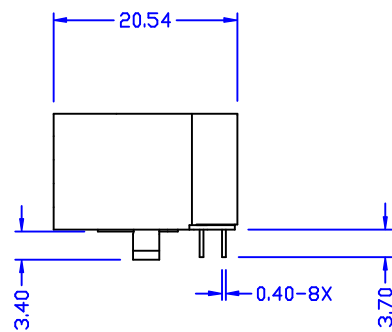
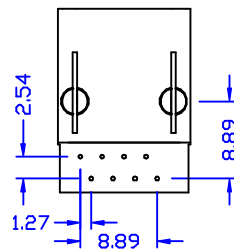
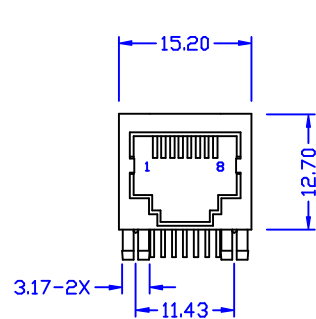
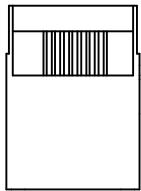
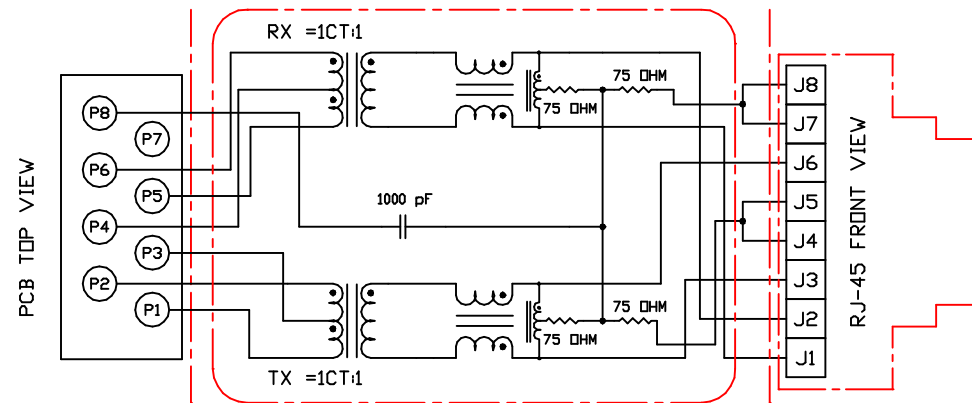




PCB LAYOUT MOUNTING HOLE(TOP VIEW)

UNLESS OTHERWISE SPECIFIED ALL TOLERANCES ARE ± 0.25

BLOCK CIRCUIT $\times 1$ SET IN ATM-073F



ELECTRICAL CHARACTERISTICS :

1. TURNS RATIO : TX = 1CT:1 $\pm 5\%$.
RX = 1CT:1 $\pm 5\%$.
2. OCL (@100KHZ/0.1V WITH 8mA DC BIAS) : 350 μ H MIN.
3. CROSS TALK : 0.3~30MHZ = -40 dB MIN.
30~60MHZ = -35 dB MIN.
60~100MHZ = -30 dB MIN.
4. HI-POT : 1500 Vrms.
5. INSERTION LOSS : 0.3~100MHZ = -1.15 dB MAX.
6. RETURN LOSS : 30MHZ = -16 dB MIN.
40MHZ = -14 dB MIN.
50MHZ = -13.5 dB MIN.
60MHZ = -13 dB MIN.
80MHZ = -10 dB MIN.
7. CMRR : 1~30MHZ = -30 dB MIN.
30~60MHZ = -25 dB MIN.
60~125MHZ = -20 dB MIN.

						Atech®		TITLE 10/100 BASE-T LAN-MATE	
						Atech Technology Co., Ltd.		DWG. NO.	
						TEL:02794-6667 FAX:02794-8004		ATM-073F	
RELEASE		09/06/2004	BART	AL	SUEDY	UNITS:		SAFETY	
NO:		DATE	BY	CHK	APPD	M/M		SHEET 10F 1	
REVISIONS						DATE		DRAW	
						09/06/04		Pair	