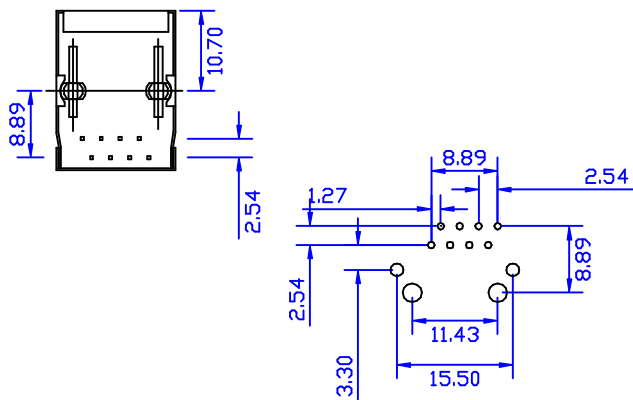
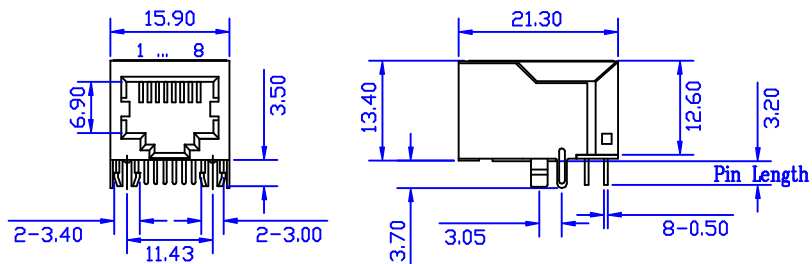
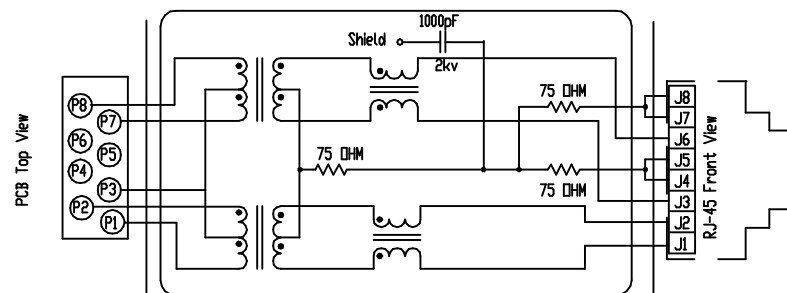




PCB LAYOUT MOUNTING HOLE(TOP VIEW)

UNLESS OTHERWISE SPECIFIED ALL TOLERANCES ARE $\pm 0.25\text{mm}$

Block Circuit x 1 set in LMIS018X



ELECTRICAL CHARACTERISTICS :

1. TURNS RATIO : 1CT:1CT $\pm 5\%$.
2. OCL (@100KHZ/0.1V WITH 8mA BIAS) : 350 μH MIN.
3. CW/W : 56 pF MAX.
4. L.L : 0.5 μH MAX.
5. DCR : P = 0.7 OHM MAX.
J = 1.1 OHM MAX.
6. HI-POT : 1500 Vrms.
7. INSERTION LOSS : 0.3~100 MHZ = -1.15 dB MAX.
8. RETURN LOSS : 30 MHZ = -20 dB MIN.
60 MHZ = -14 dB MIN.
80 MHZ = -12 dB MIN.
9. CROSSTALK : 30 MHZ = -45 dB MIN.
60 MHZ = -40 dB MIN.
100 MHZ = -35 dB MIN.
10. DIFFERENTIAL TO COMMON MODE REJECTION :
30 MHZ = -42 dB MIN.
60 MHZ = -37 dB MIN.
100 MHZ = -33 dB MIN.

						Atech® Atech Technology Co., Ltd. TEL:0212794-6667 FAX:0212794-8004		TITLE LAN MATE	
								DWG. NO. ATM-073H	
	RELEASE	01/14/2005	BART	AL	SUEDY	UNITS: M/M (INCHES)	SAFETY	SHEET 1 OF 1	
NO:	DESCRIPTION	DATE	BY	CHK	APPD	DATE 01/14/05		DRAW <i>Pair</i>	
REVISIONS									