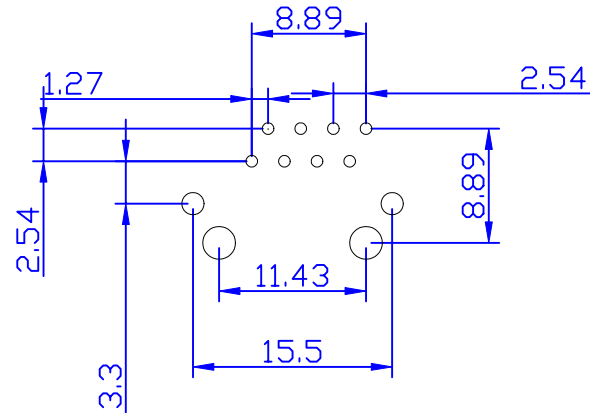
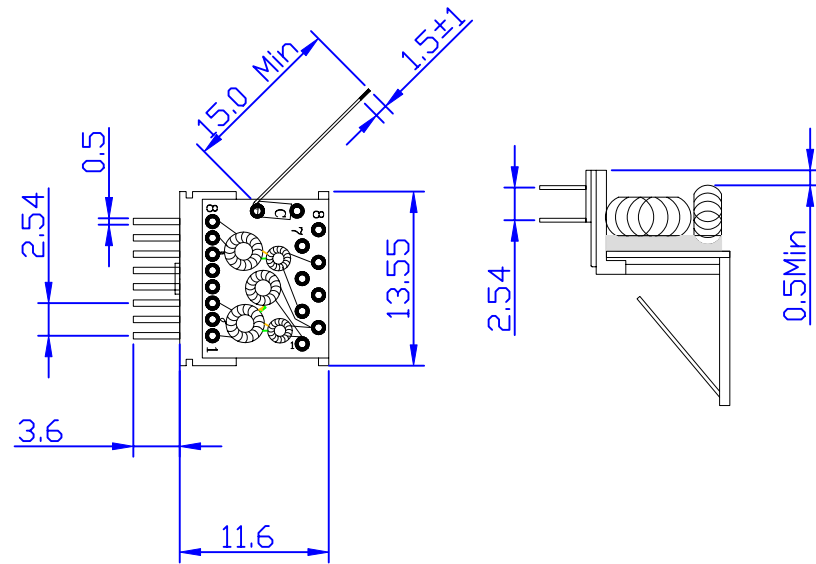
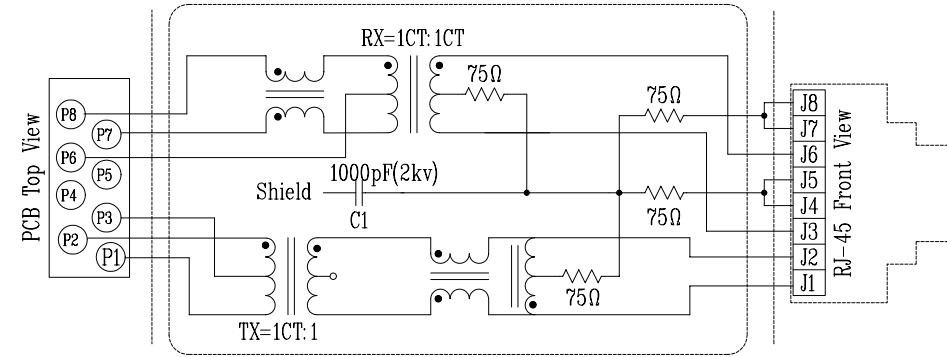




PCB LAYOUT MOUNTING HOLE (TOP VIEW)

UNLESS OTHERWISE SPECIFIED ALL TOLERANCES ARE  $\pm 0.25$



## ELECTRICAL CHARACTERISTICS :

1. OCL (@100KHZ/0.1Vrms 8mA DC/BIAS) : 350 uH MIN.
2. HI-POT : 1500 Vrms.
3. INSERTION LOSS : 1~100MHZ = -1.0 dB MAX.
4. RETURN LOSS : 1~30 MHZ = -20 dB MIN.  
30~60 MHZ = -15 dB MIN.  
60~80 MHZ = -11 dB MIN.
5. TURNS RATIO : P1-P2:J1-J2 = 1CT:1  $\pm 5\%$ .  
P7-P8:J3-J6 = 1CT:1CT  $\pm 5\%$ .
6. CROSS TALK : 1~100 MHZ = -30 dB MIN.

|           |             |            |      |     |       |                                                                                               |        |                     |  |
|-----------|-------------|------------|------|-----|-------|-----------------------------------------------------------------------------------------------|--------|---------------------|--|
|           |             |            |      |     |       | <div><b>Atech®</b><br/>Atech Technology Co., Ltd.<br/>TEL:02)2794-6667 FAX:02)2794-8004</div> |        | TITLE<br>LAN-MATE   |  |
|           |             |            |      |     |       |                                                                                               |        | DWG. NO.<br>ATM-093 |  |
|           | RELEASE     | 03/03/2005 | BART | AL  | SUEDY | UNITS:<br>mm                                                                                  | SAFETY | SHEET OF            |  |
| NO.       | DESCRIPTION | DATE       | BY   | CHK | APPD  | DATE<br>03/03/05                                                                              |        | DRAW<br>Pair        |  |
| REVISIONS |             |            |      |     |       |                                                                                               |        |                     |  |