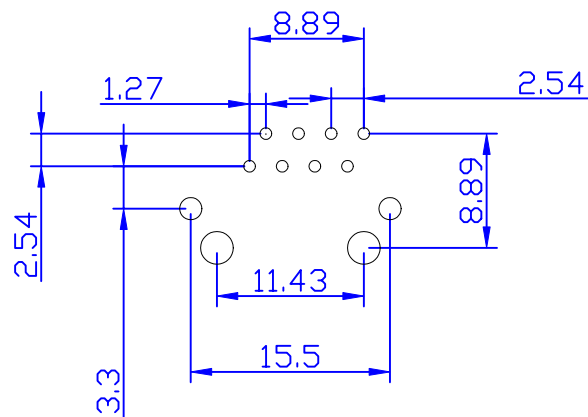
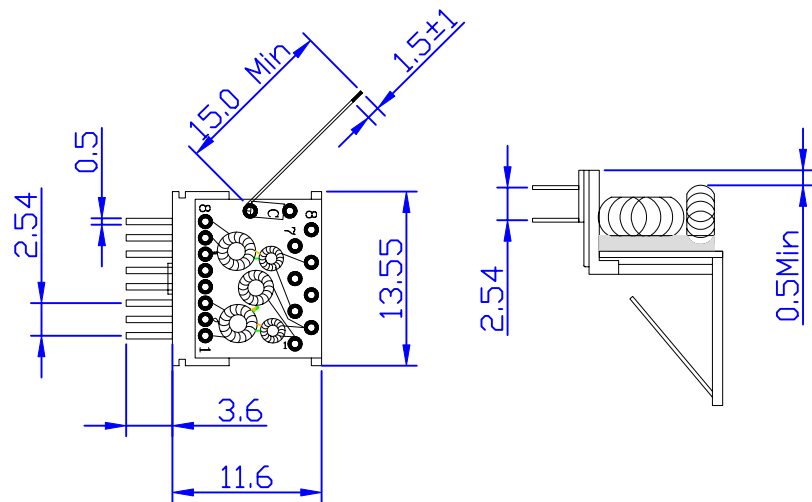
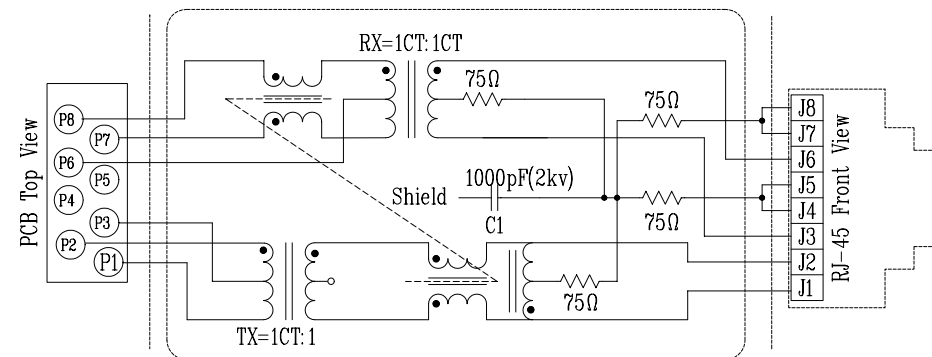




PCB LAYOUT MOUNTING HOLE (TOP VIEW)

UNLESS OTHERWISE SPECIFIED ALL TOLERANCES ARE ± 0.25



ELECTRICAL CHARACTERISTICS :

1. OCL (@100KHZ/0.1Vrms 8mA DC/BIAS) :
P1-P2, P7-P8 = 350 uH MIN.
2. HI-POT : 1500 Vrms.
3. CMRR : 1~100MHZ = -30 dB MIN.
4. INSERTION LOSS : 1~100MHZ = -1.0 dB MAX.
5. RETURN LOSS : 1~30 MHZ = -20/-20 dB MIN.
30~60 MHZ = -15/-17 dB MIN.
60~80 MHZ = -11/-15 dB MIN.
6. TURNS RATIO : P1-P2:J1-J2 = 1CT:1 $\pm 5\%$.
P7-P8:J3-J6 = 1CT:1CT $\pm 5\%$.
7. CROSS TALK : 1~100 MHZ = -30 dB MIN.

					Atech® Atech Technology Co., Ltd. TEL:02)2794-6667 FAX:02)2794-8004	TITLE LAN-MATE	
						DWG. NO. ATM-096	
	RELEASE	03/16/2005	BART	AL	SUEDY	UNITS: mm	SAFETY
NO.	DESCRIPTION	DATE	BY	CHK	APPD	DATE 03/16/05	
REVISIONS							SHEET OF DRAW Pair