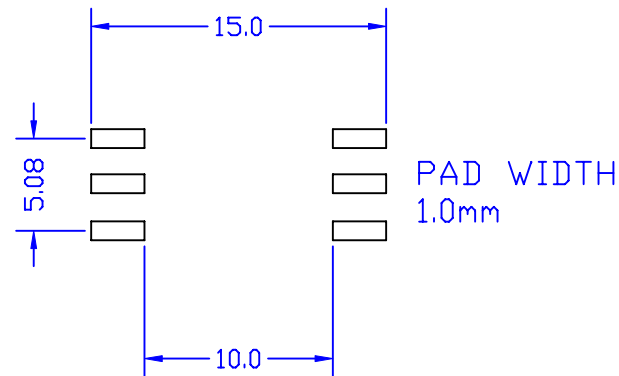
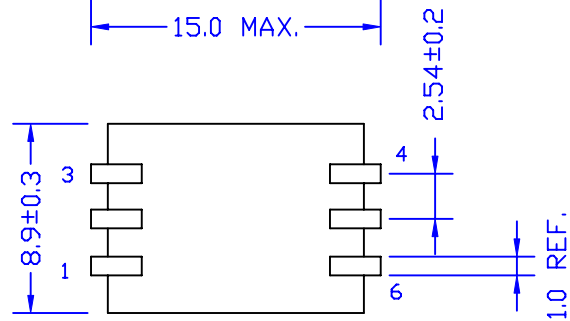
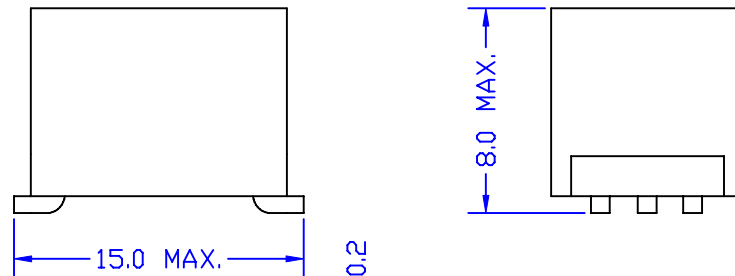
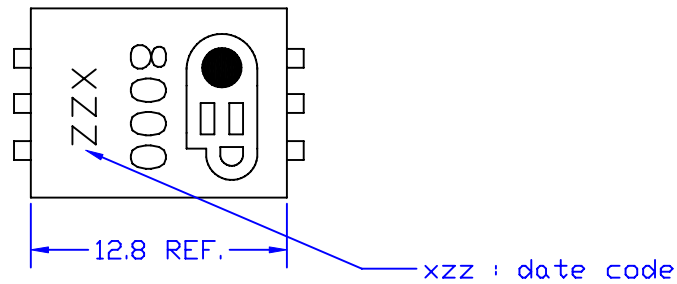
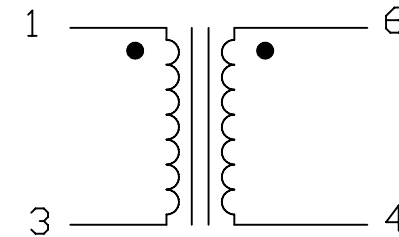




SUGGESTED PCB LAYOUT



ELECTRICAL CHARACTERISTICS :

- DCR: PIN 1-3 = 6-4 = 111 Ω $\pm$ 15%
 - IMPEDANCE MATCHING : 600 Ω TO 600 Ω
 - INDUCTANCE (@100HZ, 270mVrms, PARALLEL)
PIN 1-3 = 3.0H MIN.
 - LEAKAGE INDUCTANCE (@200HZ, 10mVrms, SERIES ,4-6 SHORT) :
PIN 1-3 = 4.1 mH NOM.
 - RETURN LOSS (REF. 600 Ω) : 200~4K HZ = 15 dB MIN.
 - INSERTION LOSS:
(REF. 600 Ω M, @2KHZ) : 4 dB MAX.
(REF. 430 Ω M, @2KHZ) : 2 dB MAX.
 - FREQ. RESPONSE (@200~4K HZ) : $\pm$ 0.25dB .
 - LONGITUDINAL BALANCE (@200~4K HZ) : 80 dB MIN.
 - TURN RATIO (@6KHZ, 0.1Vrms) 1-3:6-4 = 1 $\pm$ 1%.
 - DISTORTION (@600HZ, -10dBm) : -89dB NOM.
 - HI-POT (@3300VAC, 1mA, 1MIN.) PRI-SEC.
 - RoHS COMPLIANCE.
- NOTE : DO NOT PASS DC CURRENT THROUGH WINDINGS.

C	CHANGE CAN	06/20/2007	PAIR	MICHAEL	EUGENE	Atech® Atech Technology Co., Ltd. TEL: (02)2794-6667 FAX: (02)2794-8004	TITLE	TRANSFORMER
B	MARKING	12/14/2005	BART	MICHAEL	SUEDY		DWG. NO.	OEP8000R
A	DIM.	12/08/2005	BART	MICHAEL	SUEDY			
	RELEASE	12/06/2005	BART	MICHAEL	SUEDY	UNITS:	SAFETY	SHEET 1 OF 1
ND	DESCRIPTION	DATE	BY	CHK	APPD	M/M		
REVISIONS						DATE		DRAW
						12/06/05		Pair