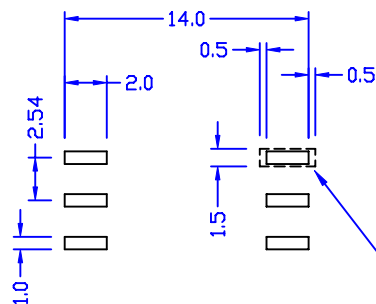
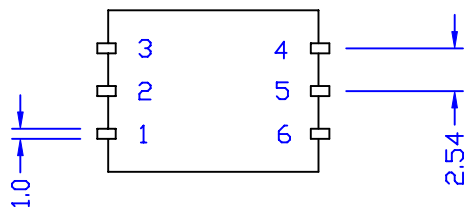
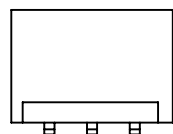
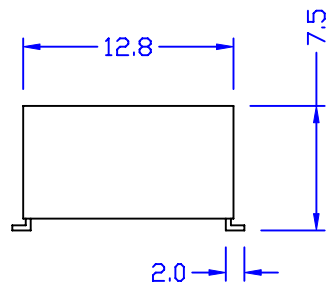
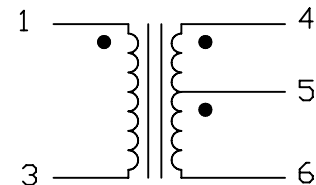
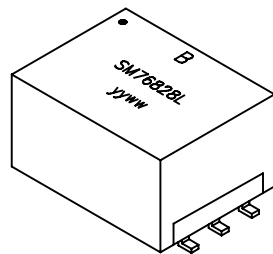
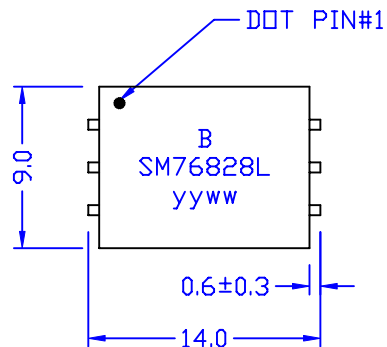




FOOT PRINET

SUGGESTED PCB LAYOUT

## ELECTRICAL CHARACTERISTICS :

- DCR : PIN 1-3 = 50 OHM  $\pm 15\%$   
PIN 4-6 = 50 OHM  $\pm 15\%$
- INDUCTANCE PARALLEL (@200HZ, 0.78Vrms) : PIN 1-3 = 1.2H MIN.  
RESISTANCE PARALLEL 2.5K OHM MIN.
- LEAKAGE INDUCTANCE (@1KHZ, 0.1Vrms) :  
PIN 1-3 (4-6 SHORT) = 3mH MAX.
- INSERTION LOSS (@2KHZ, 0dBm) :  
PIN 1-3 (4-6 W/H 600 OHM) = 1.5 dB MAX.
- FREQ. RESP (@REF 2KHZ) :  
FROM 200HZ~4KHZ =  $\pm 0.6$  dB.
- RETURN LOSS : 1-3 (4-6 W/H 600 OHM) :  
200HZ = 15 dB MIN. 4KHZ = 15dB MIN.
- T.H.D. (@600HZ, -10dBm) :  
1-3(4-6 W/H 600 OHM INPUT 600 OHM) = -65dB MAX.
- TURN RATIO (@20KHZ, 0.1Vrms) :  
PIN 1-3 : 4-6 =  $1 \pm 2\%$ .
- HI-POT (@4.6KVAC, 1mA, 1MIN) : P-S
- INSULATION RESISTANCE (@500VDC, 100M OHM MIN. 2SEC)
- RoHS COMPLIANT
- REEL PACKING

TOLERANCE :  $\pm 0.25$

|           |             |      |    |     |      |                                  |            |                       |
|-----------|-------------|------|----|-----|------|----------------------------------|------------|-----------------------|
|           |             |      |    |     |      | <b>Bourns Inductive Solution</b> |            | TITLE<br>TRANSFORMER  |
|           |             |      |    |     |      |                                  |            | DWG. NO.<br>SM76828EL |
|           |             |      |    |     |      | UNITS:<br>M/M                    | SAFETY     | SHEET 1 OF 1          |
| NO.       | DESCRIPTION | DATE | BY | CHK | APPD | DATE                             | P/N:       | DRAW                  |
| REVISIONS |             |      |    |     |      | 11/02/07                         | CTPL08007A | Pair                  |