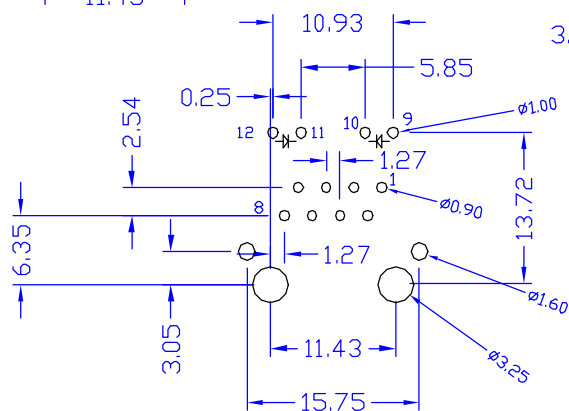
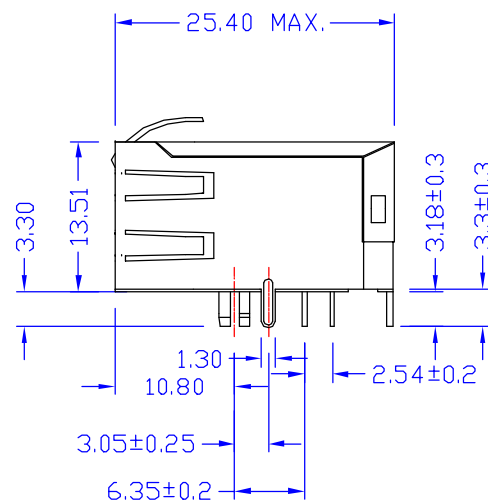
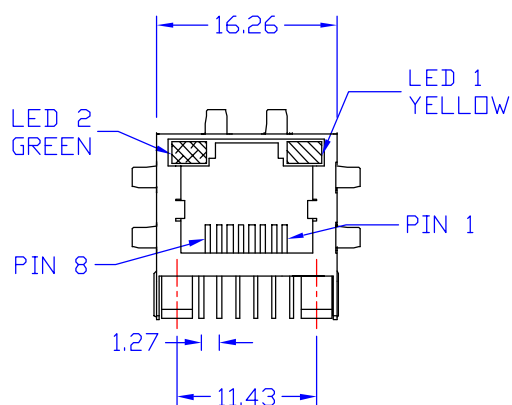
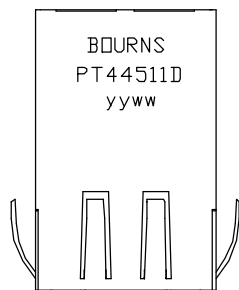
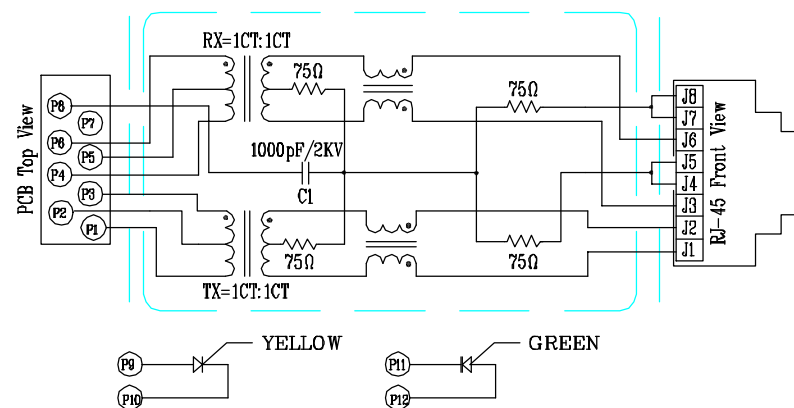




PCB LAYOUT



ELECTRICAL CHARACTERISTICS :

1. DCL (@100KHz/0.1Vrms 8mA DC/Bias) : 350 uH MIN.
2. DCR : J1-J2,J3-J6 = 1.2 OHM MAX.
3. L.L (@100KHz/0.1Vrms) : 0.3 uH MAX.
4. CW/W (@100KHz/0.1Vrms) : 30 pF MAX.
5. INSERTION LOSS : 0.1~100MHz = -1.1 dB TYP.
6. RETURN LOSS : 1~30 MHz = -18 dB MIN.
30~80 MHz = -12 dB MIN.
7. HI-PDT : 1500 Vrms.
8. CROSS TALK : 1~100MHz = -30 dB MIN.
9. TURNS RATIO : 1CT:1CT ±3%.
10. CMRR : 1~100MHz = -35 dB TYP.

UNLESS OTHERWISE SPECIFIED ALL TOLERANCES ARE ±0.25

						Atech® Atech Technology Co., Ltd. TEL:022794-6667 FAX:022794-8004	TITLE LAN-MATE SERISE		
							DWG. NO. PT44511D		
	RELEASE	05/16 /2005	BART	AL	SUEDY	UNITS: M/M	SAFETY	SHEET10F 1	
NO.	DESCRIPTION	DATE	BY	CHK	APPD	DATE			DRAW <i>Pair</i>
REVISIONS						05/16/05			