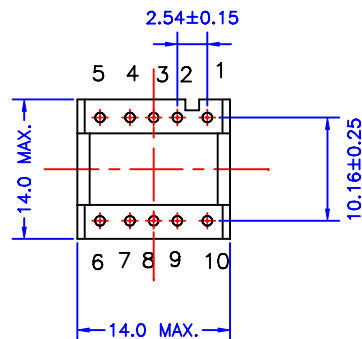
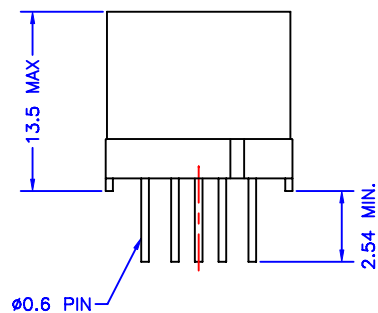
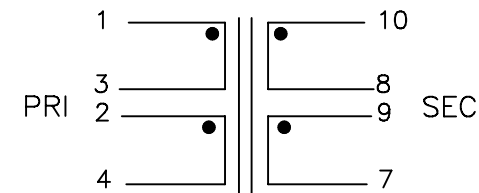


GRID 2.54 X 2.54



ELECTRICAL CHARACTERISTICS : (MEET IEC 950, FOR ADI'S AD20MSP910 IC CHIP 50366 COMPATIBLE)

- DCR : PIN 1-3 = PIN 2-4 = 1.29 OHM $\pm$ 10%.
PIN 10-8 = 1.1 OHM $\pm$ 10%.
PIN 9-7 = 1.5 OHM $\pm$ 10%.
- INDUCTANCE (@10 KHZ, 1Vrms, W/H PIN 2-3 SHORT) PIN 1-4 = 5mH $\pm$ 10%
- LEAKAGE (@100 KHZ, 100 mV, W/H PIN 2-3, 8-9, 7-10 SHORT) PIN 1-4 = 16 μ H MAX.
- Cw(@100 KHZ, 100mv W/H PIN 2-3, 8-9 SHORT) PIN 1-10 = 65 pF MAX.
- INSERTION LOSS (@100 KHZ, 0dBm, W/H PIN 2-3, 8-9 SHORT, 10-7 W/H 100 OHM)
PIN 1-4 = 0.5 dB MAX.
- FREQ RESP (@REF 100 KHZ) @30 KHZ = $\pm$ 1.1 dB.
@1.1 MHZ = $\pm$ 1.1dB.
- TURNS RATIO (@15 KHZ, 1Vrms) PIN 10-8 : 9-7 = 1 $\pm$ 1%.
PIN 10-7 : 1-4 (PIN 8-9, 3-2 SHORT)= 1 $\pm$ 1%.
- HI-POT (@1875 VAC, 1mA, 2 SEC) PRI-SEC.
- RoHS COMPLIANT

						Bourns Inductive Solution		TITLE ADSL TRANSFORMER	
B	PIN#1 NOTCH	01/05/2008	PAIR	WILLIE	EUGENE			DWG. NO.	PT539-1L
A	INSERTION LOSS (100 OHM)	09/21/2007	PAIR	WILLIE	EUGENE				
	RELEASE	09/11/2007	PAIR	WILLIE	EUGENE	UNITS: M/M		SAFETY	
NO	DESCRIPTION	DATE	BY	CHK	APPD	DATE		P/N: CTA41303RA	
REVISIONS						09/11/07		DRAW <i>Pair</i>	
								SHEET 1 OF 1	