

SAMPLES TEST REPORT

PART NO.	ATPL-167	DATE	2004/03/15								
DESCRIPTION	TRANSFORMER										
ITEM	TEST CONDITION @ 25	SAMPLES NO. TEST PIN	1	2	3	4	5	SPECIFICATION			
								Min	Typ	Max	
OCL (uH)	100KHz/0.1Vrms 8mA/DC Bias	2-3	772	720	745	728	793	350			
		5-6	775	784	726	763	744				
		8-9	766	732	750	725	771				
		11-12	771	754	775	768	765				
		14-15	760	764	750	761	771				
		17-18	736	769	746	755	736				
		20-21	808	767	752	714	743				
		23-24	746	767	756	783	767				
HI-POT(Vrms)	1500Vrms	PRI/SEC	PASS	PASS	PASS	PASS	PASS	1S			
TURNS RATIO (±5%)	1MHz/正弦波	2-3:47-46	PASS	PASS	PASS	PASS	PASS	1CT:1CT			
		5-6:44-43	PASS	PASS	PASS	PASS	PASS				
		8-9:41-40	PASS	PASS	PASS	PASS	PASS				
		11-12:38-37	PASS	PASS	PASS	PASS	PASS				
		14-15:35-34	PASS	PASS	PASS	PASS	PASS				
		17-18:32-31	PASS	PASS	PASS	PASS	PASS				
		20-21:29-28	PASS	PASS	PASS	PASS	PASS				
		23-24:26-25	PASS	PASS	PASS	PASS	PASS				
INSERTION LOSS (dB)	0.1-1MHz	TX/RX	PASS	PASS	PASS	PASS	PASS			-1.1	
	1-65MHz		PASS	PASS	PASS	PASS	PASS			-0.5	
	65-100MHz		PASS	PASS	PASS	PASS	PASS			-0.8	
	100-125MHz		PASS	PASS	PASS	PASS	PASS			-1.2	
Return Loss (dB Min) @ 100 ±15%)	0.5-40MHz	TX/RX	PASS	PASS	PASS	PASS	PASS	-18			
	40-100MHz		PASS	PASS	PASS	PASS	PASS	12-20 Log (f / 80MHz)			
CROSS TALK (dB)	0.1-100MHz	TX/RX	PASS	PASS	PASS	PASS	PASS	33+20 Log (f / 100MHz)			
CMRR (dB Min)	0.1-100MHz	TX/RX	PASS	PASS	PASS	PASS	PASS	-30			
REMARK:											